ENAE12013 *Electrical Components and Circuit Analysis*
Term 2 - 2019

Profile information current as at 12/10/2024 03:22 pm

All details in this unit profile for ENAE12013 have been officially approved by CQUniversity and represent a learning partnership between the University and you (our student).The information will not be changed unless absolutely necessary and any change will be clearly indicated by an approved correction included in the profile.

General Information

Overview
This unit introduces you to modelling electrical components and systems. You will apply theorems and network reduction techniques to DC and AC circuits, and apply problem solving techniques in the analysis of AC and DC circuits. You will also cover the concepts of transient response, and Laplace transform and transfer functions. You will apply laboratory techniques and appropriate software tools to the analysis of electrical circuits. Use fundamental electrical engineering language in context and document the process of modelling and analysis. You will present information, communicate, work and learn in a professional manner. Mix mode students must complete the practical activities by attending residential school.

Details
Career Level: *Undergraduate*
Unit Level: *Level 2*
Credit Points: *6*
Student Contribution Band: *8*
Fraction of Full-Time Student Load: *0.125*

Pre-requisites or Co-requisites
Prereq: (ENAG11002 Energy & Electricity or ENEG11009 Fundamentals of Energy & Electricity) and MATH11160 Technology Mathematics
Important note: Students enrolled in a subsequent unit who failed their pre-requisite unit, should drop the subsequent unit before the census date or within 10 working days of Fail grade notification. Students who do not drop the unit in this timeframe cannot later drop the unit without academic and financial liability. See details in the Assessment Policy and Procedure (Higher Education Coursework).

Offerings For Term 2 - 2019

- Mixed Mode

Attendance Requirements
All on-campus students are expected to attend scheduled classes – in some units, these classes are identified as a mandatory (pass/fail) component and attendance is compulsory. International students, on a student visa, must maintain a full time study load and meet both attendance and academic progress requirements in each study period (satisfactory attendance for International students is defined as maintaining at least an 80% attendance record).

Website
This unit has a website, within the Moodle system, which is available two weeks before the start of term. It is important that you visit your Moodle site throughout the term. Please visit Moodle for more information.

Class and Assessment Overview

Recommended Student Time Commitment

Each 6-credit Undergraduate unit at CQUniversity requires an overall time commitment of an average of 12.5 hours of study per week, making a total of 150 hours for the unit.

Class Timetable

Regional Campuses
Bundaberg, Cairns, Emerald, Gladstone, Mackay, Rockhampton, Townsville

Metropolitan Campuses
Adelaide, Brisbane, Melbourne, Perth, Sydney

Assessment Overview

1. **Online Quiz(zes)**
Weighting: 10%
2. **In-class Test(s)**
Weighting: 25%
3. **Practical Assessment**
Weighting: 25%
4. **Examination**
Weighting: 40%

Assessment Grading

This is a graded unit: your overall grade will be calculated from the marks or grades for each assessment task, based on the relative weightings shown in the table above. You must obtain an overall mark for the unit of at least 50%, or an overall grade of 'pass' in order to pass the unit. If any 'pass/fail' tasks are shown in the table above they must also be completed successfully ('pass' grade). You must also meet any minimum mark requirements specified for a particular assessment task, as detailed in the 'assessment task' section (note that in some instances, the minimum mark for a task may be greater than 50%). Consult the University's Grades and Results Policy for more details of interim results and final grades.

CQUniversity Policies

All University policies are available on the CQUniversity Policy site.
You may wish to view these policies:

- Grades and Results Policy
- Assessment Policy and Procedure (Higher Education Coursework)
- Review of Grade Procedure
- Student Academic Integrity Policy and Procedure
- Monitoring Academic Progress (MAP) Policy and Procedure – Domestic Students
- Monitoring Academic Progress (MAP) Policy and Procedure – International Students
- Student Refund and Credit Balance Policy and Procedure
- Student Feedback – Compliments and Complaints Policy and Procedure
- Information and Communications Technology Acceptable Use Policy and Procedure

This list is not an exhaustive list of all University policies. The full list of University policies are available on the CQUniversity Policy site.

Previous Student Feedback

Feedback, Recommendations and Responses

Every unit is reviewed for enhancement each year. At the most recent review, the following staff and student feedback items were identified and recommendations were made.

Feedback from Unit evaluation

Feedback
Residential school activities were highly appreciated.

Recommendation
Continue to offer a residential school with similar features.

Feedback from Unit evaluation

Feedback
Assignment turnaround and feedback was excellent.

Recommendation
Continue with these good practices.

Feedback from Unit evaluation

Feedback
Lecturer was helpful.

Recommendation
Maintain similar type of engagement.

Feedback from In Class

Feedback
Some students found the derivation of theorems which involves a little bit of complex mathematics, boring.

Recommendation
Keep derivation of theorems in a separate video/lecture (as an optional resource).

Unit Learning Outcomes

On successful completion of this unit, you will be able to:

1. Solve electrical circuits using fundamental electrical theorems
2. Analyse and solve problems of Direct Current (DC) circuits using network reduction techniques
3. Model and analyse the transient behaviour of circuits with resistors, inductors and capacitors
4. Analyse and solve problems of Alternating Current (AC) circuits
5. Solve electrical circuit problems using S-domain circuit analysis techniques
6. Use appropriate software tools to simulate electrical circuits and verify the results by conducting laboratory experiments using safe work practices
7. Work individually and in a team to solve electrical circuit problems and produce professional laboratory documents.

The Learning Outcomes for this unit are linked with Engineers Australia's Stage 1 Competency Standard for Professional Engineers.

Alignment of Learning Outcomes, Assessment and Graduate Attributes

— N/A Level • Introductory Level • Intermediate Level • Graduate Level ◦ Professional Level ◦ Advanced Level

Alignment of Assessment Tasks to Learning Outcomes

Assessment Tasks	Learning Outcomes						
	1	2	3	4	5	6	7
1 - In-class Test(s) - 25%	•	•	•				
2 - Online Quiz(zes) - 10%	•	•		•	•		
3 - Practical Assessment - 25%						•	•
4 - Examination - 40%			•	•	•		

Alignment of Graduate Attributes to Learning Outcomes

Graduate Attributes	Learning Outcomes						
	1	2	3	4	5	6	7
1 - Communication							•
2 - Problem Solving	•	•	•	•	•	•	
3 - Critical Thinking	•	•	•	•	•	•	
4 - Information Literacy							
5 - Team Work						•	•
6 - Information Technology Competence						•	
7 - Cross Cultural Competence							
8 - Ethical practice						•	
9 - Social Innovation							
10 - Aboriginal and Torres Strait Islander Cultures							

Alignment of Assessment Tasks to Graduate Attributes

Assessment Tasks	Graduate Attributes									
	1	2	3	4	5	6	7	8	9	10
1 - In-class Test(s) - 25%		•	•							
2 - Online Quiz(zes) - 10%		•	•							
3 - Practical Assessment - 25%	•	•	•			•				
4 - Examination - 40%		•	•							

Textbooks and Resources

Textbooks

ENAE12013

Prescribed

Introductory Circuit Analysis
13th (GLOBAL Edition) (2016)
Authors: Boylestad, RL
Pearson
Upper Saddle River , NJ , USA
ISBN: 9781292098951
Binding: Paperback

Additional Textbook Information
Note: The downloadable eBook is available as an online purchase via this link:
http://www.pearson.com.au/9781292098968
However, if you prefer a paper text, you can purchase from the CQUni Bookshop here: http://bookshop.cqu.edu.au (search on the Unit code)

View textbooks at the CQUniversity Bookshop

IT Resources

You will need access to the following IT resources:

- CQUniversity Student Email
- Internet
- Unit Website (Moodle)

Referencing Style

All submissions for this unit must use the referencing style: Harvard (author-date)
For further information, see the Assessment Tasks.

Teaching Contacts

Shaminda De Silva Unit Coordinator
s.desilva@cqu.edu.au

Schedule

Week 1 - 15 Jul 2019

Module/Topic	Chapter	Events and Submissions/Topic
Electrical Components and Modelling their Electrical Characteristics	1-7	

Week 2 - 22 Jul 2019

Module/Topic	Chapter	Events and Submissions/Topic
Nodal Analysis	8	**Online Quiz 1 : (**Progressive Test 1): Due Sunday 11:45 pm AEST

Week 3 - 29 Jul 2019

Module/Topic	Chapter	Events and Submissions/Topic
Mesh Analysis	8	**Lab Quiz 1**: Due Sunday 11:45 pm AEST

Week 4 - 05 Aug 2019

Module/Topic	Chapter	Events and Submissions/Topic
Network Theorems	9	**Online Quiz 2:** (Progressive Test 2): Due Sunday 11:45pm AEST

Week 5 - 12 Aug 2019

Module/Topic	Chapter	Events and Submissions/Topic
Capacitors and Inductors	10-12	**Lab Quiz 2** : Due Sunday 11:45 pm AEST

Vacation Week - 19 Aug 2019

Module/Topic	Chapter	Events and Submissions/Topic

Week 6 - 26 Aug 2019

Module/Topic	Chapter	Events and Submissions/Topic
Transient Behaviour of Electrical Components	10-12	Residential school 26 - 28, August. (Monday, Tuesday and Wednesday) **In-Class test** : Due during Residential school

Week 7 - 02 Sep 2019

Module/Topic	Chapter	Events and Submissions/Topic
Complex Numbers and Phasors	13-14	**Online Quiz 3:** (Progressive Test 3) : Due Sunday 11:45 pm AEST

Week 8 - 09 Sep 2019

Module/Topic	Chapter	Events and Submissions/Topic
Steady State Sinusoidal Analysis I	15-17	**Lab Quiz 3:** Due Sunday 11:45 pm AEST

Week 9 - 16 Sep 2019

Module/Topic	Chapter	Events and Submissions/Topic
Steady State Sinusoidal Analysis II	17-19	**Lab Quiz 4** : Due Sunday 11:45 pm AEST

Week 10 - 23 Sep 2019

Module/Topic	Chapter	Events and Submissions/Topic
Power in AC circuits	20	**Online Quiz 4** : (Progressive Test 4) Due Sunday 11:45 pm AEST

Week 11 - 30 Sep 2019

Module/Topic	Chapter	Events and Submissions/Topic
Laplace Transforms	Unit Resource Online	**Practical and written assessment** Due: Week 11 Monday (30 Sept 2019) 7:00 am AEST

Week 12 - 07 Oct 2019

Module/Topic	Chapter	Events and Submissions/Topic
Circuit Analysis in s Domain	Unit Resource Online	

Review/Exam Week - 14 Oct 2019

Module/Topic	Chapter	Events and Submissions/Topic
Review	All	

Exam Week - 21 Oct 2019

Module/Topic	Chapter	Events and Submissions/Topic

Term Specific Information

Residential school attendance is compulsory to complete the laboratory exercises and in-class test.
Students who are unable to attend the residential school should consider withdrawing from the unit before the census date.

Assessment Tasks

1 Online Quizzes (Progressive Tests)

Assessment Type
Online Quiz(zes)

Task Description
The assessment is a set of online Progressive tests which can be accessed via the unit Moodle site. The progressive tests are an integrated part of the study to test on the key concepts f each week. Details of the assessment can be found on the unit Moodle site at the beginning of the term. Each Progressive test will be available up to 2 weeks after the relevant week to allow students who cannot find time each week for study. For example Progressive test one will close at the end of week 2 and Progressive test 2 will close at end of week 5. All progressive tests must be completed by the end of week 10.
Each progressive test can be attempted several times, but the score for the Progressive test will be score for your first attempt. Correct answer for the Progressive test questions will be available immediately after you submit your answers. If you encounter any network access issues during the Progressive test, the unit coordinator should be notified at your earliest convenient.

Number of Quizzes
4

Frequency of Quizzes

Assessment Due Date

Suggested date and time will be notified and confirmed via the unit Moodle site

Return Date to Students

Results are available immediately after completion of each Progressive test

Weighting
10%

Minimum mark or grade
50%

Assessment Criteria
Students must need to satisfactorily score more than 50% for the Online quizzes (Progressive test) to pass this unit.

Referencing Style

- Harvard (author-date)

Submission
Online

assessmentOutcome

- Solve electrical circuits using fundamental electrical theorems
- Analyse and solve problems of Direct Current (DC) circuits using network reduction techniques
- Analyse and solve problems of Alternating Current (AC) circuits
- Solve electrical circuit problems using S-domain circuit analysis techniques

assessmentGradAttribute

- Problem Solving
- Critical Thinking

2 In-class test

Assessment Type
In-class Test(s)

Task Description
This assessment covers weekly topics from Week 1 to Week 5. Students are required to answer analytical and numerical questions to demonstrate their theoretical knowledge and analytical problem solving skills.
Students will sit this in-class test **during Residential school** in **Rockhampton**.
Further information about In-class test will be provided in the unit Moodle site.

Assessment Due Date

Suggested date and time will be confirmed and notified via the unit Moodle site

Return Date to Students

We strive to release the assessment marks in 2 weeks after due date

Weighting
25%

Minimum mark or grade
25%

Assessment Criteria
In-class test will be graded using the following criteria:

- Correct answers
- All working must be shown
- Answers must be neat, tidy and legible
- Correct format
- Application of correct methods and procedures
- Discussion of answers
- All questions must be attempted
- Each question in the test will be assessed separately.
- Need to score more than 25% to PASS

Referencing Style

- Harvard (author-date)

Submission
Offline

assessmentOutcome

- Solve electrical circuits using fundamental electrical theorems
- Analyse and solve problems of Direct Current (DC) circuits using network reduction techniques
- Model and analyse the transient behaviour of circuits with resistors, inductors and capacitors

assessmentGradAttribute

- Problem Solving
- Critical Thinking

3 Practical and written assessment

Assessment Type
Practical Assessment

Task Description
This assessment item covers all topics.
Laboratories will be held at the residential school. All information regarding the laboratories will be provided to the students via the unit Moodle site at the start of the term.
Laboratory **attendance is compulsory** and all students **must pass** the laboratory exercise assessment in order to pass the unit. Laboratory reports of students who did not attend the laboratory sessions will not be accepted for marking and will be awarded zero marks.
Students need to answer a set of online quizzes to complete this assessment task.
Although students will be working in teams during the laboratory sessions, each student must **submit an individual**

laboratory report (not one report per laboratory group) by the due date.

Assessment Due Date
Week 11 Monday (30 Sept 2019) 7:00 am AEST

Return Date to Students
Exam Week Monday (21 Oct 2019)

We strive to release the assessment marks in 2 weeks after due date

Weighting
25%

Minimum mark or grade
50%

Assessment Criteria
Laboratory exercises will be graded using the following criteria,

- Correct answers
- All working must be shown
- Report must be neat, tidy and legible
- Correct format
- Correct description of laboratory procedures
- Correct interpretation and discussion of laboratory results
- All laboratory exercises must be attempted
- Laboratory exercise based assessment marks need to be more than 50% to PASS

Referencing Style

- Harvard (author-date)

Submission
Online

assessmentOutcome

- Use appropriate software tools to simulate electrical circuits and verify the results by conducting laboratory experiments using safe work practices
- Work individually and in a team to solve electrical circuit problems and produce professional laboratory documents.

assessmentGradAttribute

- Communication
- Problem Solving
- Critical Thinking
- Information Technology Competence

Examination

Outline
Complete an invigilated examination.

Date
During the examination period at a CQUniversity examination centre.

Weighting
40%

Length
180 minutes

Minimum mark or grade
50%

Exam Conditions
Restricted.

Materials
Dictionary - non-electronic, concise, direct translation only (dictionary must not contain any notes or comments).
Calculator - all non-communicable calculators, including scientific, programmable and graphics calculators are

authorised

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assessmentOutcome

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assessmentGradAttribute

Academic Integrity Statement

As a CQUniversity student you are expected to act honestly in all aspects of your academic work.

Any assessable work undertaken or submitted for review or assessment must be your own work. Assessable work is any type of work you do to meet the assessment requirements in the unit, including draft work submitted for review and feedback and final work to be assessed.

When you use the ideas, words or data of others in your assessment, you must thoroughly and clearly acknowledge the source of this information by using the correct referencing style for your unit. Using others' work without proper acknowledgement may be considered a form of intellectual dishonesty.

Participating honestly, respectfully, responsibly, and fairly in your university study ensures the CQUniversity qualification you earn will be valued as a true indication of your individual academic achievement and will continue to receive the respect and recognition it deserves.

As a student, you are responsible for reading and following CQUniversity's policies, including the **Student Academic Integrity Policy and Procedure**. This policy sets out CQUniversity's expectations of you to act with integrity, examples of academic integrity breaches to avoid, the processes used to address alleged breaches of academic integrity, and potential penalties.

What is a breach of academic integrity?
A breach of academic integrity includes but is not limited to plagiarism, self-plagiarism, collusion, cheating, contract cheating, and academic misconduct. The Student Academic Integrity Policy and Procedure defines what these terms mean and gives examples.

Why is academic integrity important?
A breach of academic integrity may result in one or more penalties, including suspension or even expulsion from the University. It can also have negative implications for student visas and future enrolment at CQUniversity or elsewhere. Students who engage in contract cheating also risk being blackmailed by contract cheating services.

Where can I get assistance?
For academic advice and guidance, the Academic Learning Centre (ALC) can support you in becoming confident in completing assessments with integrity and of high standard.

What can you do to act with integrity?

Be Honest
If your assessment task is done by someone else, it would be dishonest of you to claim it as your own

Seek Help
If you are not sure about how to cite or reference in essays, reports etc, then seek help from your lecturer, the library or the Academic Learning Centre (ALC)

Produce Original Work
Originality comes from your ability to read widely, think critically, and apply your gained knowledge to address a question or problem