



ENEE13018 *Analogue Electronics*

Term 2 - 2019

Profile information current as at 05/05/2024 08:53 am

All details in this unit profile for ENEE13018 have been officially approved by CQUniversity and represent a learning partnership between the University and you (our student). The information will not be changed unless absolutely necessary and any change will be clearly indicated by an approved correction included in the profile.

General Information

Overview

This unit introduces the operating principles of basic analogue electronic elements such as diodes and transistors. You will then use these basic elements to construct analogue devices such as amplifiers, active filters, oscillators and regulated power supplies. You will also apply laboratory techniques and appropriate software tools to the analysis of analogue electronic circuits. You will use fundamental electrical engineering language in context and document the process of design, modelling and analysis. You will present information, communicate, work and learn in a professional manner. On satisfactory completion you should be able to research and discuss the application of analogue electronics devices in different applications such as signal processing and communication. Distance students achieve practical activities outcomes by attending a residential school.

Details

Career Level: *Undergraduate*

Unit Level: *Level 3*

Credit Points: 6

Student Contribution Band: 8

Fraction of Full-Time Student Load: 0.125

Pre-requisites or Co-requisites

Prerequisites: (ENEG11009 Fundamentals of Energy and Electricity OR PHYS11185 Engineering Physics B OR ENAG11002 Energy and Electricity) AND (MATH11218 Applied Mathematics OR MATH11160 Technology Mathematics)

Important note: Students enrolled in a subsequent unit who failed their pre-requisite unit, should drop the subsequent unit before the census date or within 10 working days of Fail grade notification. Students who do not drop the unit in this timeframe cannot later drop the unit without academic and financial liability. See details in the [Assessment Policy and Procedure \(Higher Education Coursework\)](#).

Offerings For Term 2 - 2019

- Bundaberg
- Cairns
- Gladstone
- Mackay
- Mixed Mode
- Rockhampton

Attendance Requirements

All on-campus students are expected to attend scheduled classes – in some units, these classes are identified as a mandatory (pass/fail) component and attendance is compulsory. International students, on a student visa, must maintain a full time study load and meet both attendance and academic progress requirements in each study period (satisfactory attendance for International students is defined as maintaining at least an 80% attendance record).

Residential Schools

This unit has a Compulsory Residential School for distance mode students and the details are:

Click here to see your [Residential School Timetable](#).

Website

[This unit has a website, within the Moodle system, which is available two weeks before the start of term. It is important that you visit your Moodle site throughout the term. Please visit Moodle for more information.](#)

Class and Assessment Overview

Recommended Student Time Commitment

Each 6-credit Undergraduate unit at CQUniversity requires an overall time commitment of an average of 12.5 hours of study per week, making a total of 150 hours for the unit.

Class Timetable

[Regional Campuses](#)

Bundaberg, Cairns, Emerald, Gladstone, Mackay, Rockhampton, Townsville

[Metropolitan Campuses](#)

Adelaide, Brisbane, Melbourne, Perth, Sydney

Assessment Overview

1. **Written Assessment**

Weighting: Pass/Fail

2. **Written Assessment**

Weighting: 15%

3. **Practical and Written Assessment**

Weighting: 30%

4. **Written Assessment**

Weighting: 15%

5. **Examination**

Weighting: 40%

Assessment Grading

This is a graded unit: your overall grade will be calculated from the marks or grades for each assessment task, based on the relative weightings shown in the table above. You must obtain an overall mark for the unit of at least 50%, or an overall grade of 'pass' in order to pass the unit. If any 'pass/fail' tasks are shown in the table above they must also be completed successfully ('pass' grade). You must also meet any minimum mark requirements specified for a particular assessment task, as detailed in the 'assessment task' section (note that in some instances, the minimum mark for a task may be greater than 50%). Consult the [University's Grades and Results Policy](#) for more details of interim results and final grades.

CQUniversity Policies

All University policies are available on the [CQUniversity Policy site](#).

You may wish to view these policies:

- Grades and Results Policy
- Assessment Policy and Procedure (Higher Education Coursework)
- Review of Grade Procedure
- Student Academic Integrity Policy and Procedure
- Monitoring Academic Progress (MAP) Policy and Procedure – Domestic Students
- Monitoring Academic Progress (MAP) Policy and Procedure – International Students
- Student Refund and Credit Balance Policy and Procedure
- Student Feedback – Compliments and Complaints Policy and Procedure
- Information and Communications Technology Acceptable Use Policy and Procedure

This list is not an exhaustive list of all University policies. The full list of University policies are available on the [CQUniversity Policy site](#).

Previous Student Feedback

Feedback, Recommendations and Responses

Every unit is reviewed for enhancement each year. At the most recent review, the following staff and student feedback items were identified and recommendations were made.

Feedback from 'Have Your Say' feedback. Individual emails.

Feedback

Many positive responses received that echoed the value of the newly trialed Q&A sessions as well as the analysis/design tutorial sessions.

Recommendation

Continue with the analysis/design tutorials. Also, continue refining the Q&A topics and content.

Feedback from 'Have Your Say' feedback.

Feedback

Workload distribution (especially first half of the term) and also the lab reports (where no lab partner is available).

Recommendation

Investigate moving some of the final theory topics into the Laboratory exercises to make more space (time) for the foundational topics in the early weeks. Facilitate lab team formation early in the term and devise a reduced lab report specification for students without a lab partner.

Unit Learning Outcomes

On successful completion of this unit, you will be able to:

1. Research the operation of semiconductor devices and identify their practical applications.
2. Analyse the operation and application of analogue electronics devices in various applications.
3. Design the interface and power supply arrangements for typical electronic systems.
4. Analyse the behaviour of electronic circuits using computer-aided tools.
5. Interpret function requirements, evaluate design options and prepare project documents for typical applications.
6. Construct electronic circuits to a given design and validate their operation.
7. Interpret and use electronic engineering terminologies and symbols that conform to Australian Standards.
8. Work collaboratively and autonomously to solve problems and record and communicate clearly and professionally the approach used to solve problems.

The Learning Outcomes for this unit are linked with Engineers Australia's **Stage 1 Competency Standard for Professional Engineers**, **Stage 1 Competency Standard for Engineering Technologists** and **Stage 1 Competency Standard for Engineering Associates**.

Alignment of Learning Outcomes, Assessment and Graduate Attributes



Alignment of Assessment Tasks to Learning Outcomes

Assessment Tasks	Learning Outcomes							
	1	2	3	4	5	6	7	8
1 - Written Assessment - 0%	•	•	•				•	•
2 - Written Assessment - 15%	•	•	•				•	•

Assessment Tasks	Learning Outcomes							
	1	2	3	4	5	6	7	8
3 - Practical and Written Assessment - 30%	•	•	•	•	•	•	•	•
4 - Written Assessment - 15%	•	•	•		•	•	•	•
5 - Examination - 40%	•	•	•	•	•	•	•	•

Alignment of Graduate Attributes to Learning Outcomes

Graduate Attributes	Learning Outcomes							
	1	2	3	4	5	6	7	8
1 - Communication	•	•	•	•	•	•	•	•
2 - Problem Solving	•	•	•	•	•	•	•	•
3 - Critical Thinking	•	•	•	•	•	•	•	•
4 - Information Literacy	•	•	•	•	•	•	•	•
5 - Team Work								
6 - Information Technology Competence	•	•	•	•	•	•		
7 - Cross Cultural Competence								
8 - Ethical practice								
9 - Social Innovation								
10 - Aboriginal and Torres Strait Islander Cultures								

Alignment of Assessment Tasks to Graduate Attributes

Assessment Tasks	Graduate Attributes									
	1	2	3	4	5	6	7	8	9	10
1 - Written Assessment - 0%	•	•	•	•		•				
2 - Written Assessment - 15%	•	•	•	•		•				
3 - Practical and Written Assessment - 30%	•	•	•	•	•	•	•	•		
4 - Written Assessment - 15%	•	•	•	•		•				
5 - Examination - 40%	•	•	•					•		

Textbooks and Resources

Textbooks

ENEE13018

Prescribed

Electronic Devices (Conventional Current Version)

Edition: 9th edn (2014)

Authors: Floyd, Thomas L

Pearson

Harlow , Essex , UK

Binding: eBook

Additional Textbook Information

The paper version of this book is now Out of Print. See your Moodle site for a link to the eBook.

IT Resources

You will need access to the following IT resources:

- CQUniversity Student Email
- Internet
- Unit Website (Moodle)
- Microsoft Excel or Equivalent Spreadsheet Application
- Multisim 14.0 Software by National Instruments (Education Edition preferred). NB - CQU has an agreement with NI which allows students to use uni licenses from home.

Referencing Style

All submissions for this unit must use the referencing style: [Harvard \(author-date\)](#)

For further information, see the Assessment Tasks.

Teaching Contacts

Piet Janse Van Rensburg Unit Coordinator

p.jansevanrensburg@cqu.edu.au

Schedule

Week 1 - 15 Jul 2019

Module/Topic	Chapter	Events and Submissions/Topic
Semiconductors; Diode Applications	1 & 2	

Week 2 - 22 Jul 2019

Module/Topic	Chapter	Events and Submissions/Topic
Diode Applications; Specialised Diodes	2 & 3	

Week 3 - 29 Jul 2019

Module/Topic	Chapter	Events and Submissions/Topic
Introduction to the BJT; BJT Bias Circuits	4 & 5	Workbook 1 Due Wednesday 11:00 PM AEST

Week 4 - 05 Aug 2019

Module/Topic	Chapter	Events and Submissions/Topic
Transistor Amplifiers	6	

Week 5 - 12 Aug 2019		
Module/Topic	Chapter	Events and Submissions/Topic
Power Amplifiers	7	ASSIGNMENT 1 Due: Week 5 Wednesday (14 Aug 2019) 11:00 pm AEST
Vacation Week - 19 Aug 2019		
Module/Topic	Chapter	Events and Submissions/Topic
Week 6 - 26 Aug 2019		
Module/Topic	Chapter	Events and Submissions/Topic
Field-Effect Transistors	8	Distance (MIX) Students - Res School: Thursday and Friday (29-30 Aug '19), 08:00 am - 16:00 pm. (No Res School on the Saturday) Rockhampton North, Building 28 - Room 2.13 (Electronics Lab).
Week 7 - 02 Sep 2019		
Module/Topic	Chapter	Events and Submissions/Topic
FET Amplifiers	9	Laboratory Report - Exercises 1, 2 and 3 - Due Wednesday 11:00 PM AEST
Week 8 - 09 Sep 2019		
Module/Topic	Chapter	Events and Submissions/Topic
FET and BJT Amplifier Frequency Response	10	Workbook 2 Due Wednesday 11:00 PM AEST
Week 9 - 16 Sep 2019		
Module/Topic	Chapter	Events and Submissions/Topic
The Operational Amplifier	12 & 13	
Week 10 - 23 Sep 2019		
Module/Topic	Chapter	Events and Submissions/Topic
Some Special Operational Amplifier Circuits (incl. Filters)	Portions of Chapters 14,15 and 16	ASSIGNMENT 2 Due: Week 10 Wednesday (25 Sept 2019) 11:00 pm AEST
Week 11 - 30 Sep 2019		
Module/Topic	Chapter	Events and Submissions/Topic
Voltage Regulators	17	Laboratory Report - Exercises 4, 5, 6 - Due Wednesday 11:00 PM AEST
Week 12 - 07 Oct 2019		
Module/Topic	Chapter	Events and Submissions/Topic
Thermal Design	Supplementary Notes (Moodle)	
Review/Exam Week - 14 Oct 2019		
Module/Topic	Chapter	Events and Submissions/Topic
		Examination - official exam time table to be released in due course.

Assessment Tasks

1 WORKBOOK

Assessment Type

Written Assessment

Task Description

This assessment item covers all the topics.

Teamwork is encouraged, however only solutions that **you were directly involved in**, may be submitted (i.e. submission of solutions done by your team where you were not involved, equates to plagiarism).

Workbook questions will be available from the unit website three weeks before the due date. They consist of selected questions from the unit textbook and other sources.

Students are expected to attempt workbook questions as part of their weekly workload and students are to submit them periodically as specified on the unit website.

To prevent electronic plagiarism, **typed workbook submissions are not acceptable**. Students should scan clear and legible hand written workbook problems for online submission as a **PDF** file.

Assessment Due Date

Workbook 1 - Wednesday Week 3 (11:00 PM); and Workbook 2 - Wednesday Week 8 (11:00 PM)

Return Date to Students

We strive to return assessments to students within 2 weeks.

Weighting

Pass/Fail

Minimum mark or grade

Pass/Fail - You need to properly attempt at least 80% of each chapter's issued workbook problems in order to pass the unit.

Assessment Criteria

The workbook will be graded Pass/Fail using the following criteria:

- Correct analysis and thinking;
- Drawings of circuits, graphs and waveforms;
- Correct answers and units - confirmation of answers by simulation with NI Multisim;
- All working and intermediate steps must be shown with justification of steps taken;
- Work must be neat, tidy and legible;

You need to **properly attempt** at least 80% of the workbook questions to pass the workbook.

Referencing Style

- [Harvard \(author-date\)](#)

Submission

Online

Submission Instructions

1) Plagiarism statement and 2) complete hand-written work scanned in together as one .pdf file

Learning Outcomes Assessed

- Research the operation of semiconductor devices and identify their practical applications.
- Analyse the operation and application of analogue electronics devices in various applications.
- Design the interface and power supply arrangements for typical electronic systems.
- Interpret and use electronic engineering terminologies and symbols that conform to Australian Standards.
- Work collaboratively and autonomously to solve problems and record and communicate clearly and professionally the approach used to solve problems.

Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Literacy
- Information Technology Competence

2 ASSIGNMENT 1

Assessment Type

Written Assessment

Task Description

This assessment item covers Topics 1-4.

Individual work is mandatory - this is a take-home test. None of your steps or solutions may be discussed or divulged to a fellow student.

Please refer to the CQU plagiarism policy - a **signed cover page declaring individual work** is required.

The assignment questions will be released on the unit website at least 2 weeks before the assignment is due to be submitted.

To prevent electronic plagiarism, **typed submissions are not acceptable**. Students should scan clear and legible hand written work for online submission as a **PDF** file.

Assessment Due Date

Week 5 Wednesday (14 Aug 2019) 11:00 pm AEST

Return Date to Students

We strive to return assessments to students within 2 weeks.

Weighting

15%

Assessment Criteria

The assignments will be graded using the following criteria:

- Correct analysis and thinking;
- Drawings of circuits, graphs and waveforms;
- Correct answers and units - confirmation of answers by simulation with NI Multisim;
- All working and intermediate steps must be shown with justification of steps taken;
- Assignments must be tidy and legible;
- All questions must be attempted.

Referencing Style

- [Harvard \(author-date\)](#)

Submission

Online

Submission Instructions

1) Plagiarism statement and 2) complete hand-written assignment scanned in together as one .pdf file

Learning Outcomes Assessed

- Research the operation of semiconductor devices and identify their practical applications.
- Analyse the operation and application of analogue electronics devices in various applications.
- Design the interface and power supply arrangements for typical electronic systems.
- Interpret and use electronic engineering terminologies and symbols that conform to Australian Standards.
- Work collaboratively and autonomously to solve problems and record and communicate clearly and professionally the approach used to solve problems.

Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Literacy
- Information Technology Competence

3 LABORATORY EXERCISES

Assessment Type

Practical and Written Assessment

Task Description

This assessment item covers most topics of this unit.

Team work is encouraged (**2 persons per team**), however **only ONE combined report** needs to be submitted by BOTH students. Each student should submit a personal **signed cover page declaring the team work done**, specifying the other team member's name.

Team reports must be **professional and typed**, including references. In cases where an individual student cannot conveniently join up to form a team, a slightly reduced report specification will be issued.

Photographic evidence is required to prove that electronic circuits were constructed and measurements were obtained by all team members - for this reason it is required that all team members show their hands or fingers in all

photographs as a 'signature'.

(For national and international accreditation, we get audited by Engineers Australia, and this photographic proof makes it easy to satisfy the auditors that each student has fully participated in all the Labs - a very important item in their list of requirements.)

Laboratory sessions will be held at various times, as directed by the unit website, through the semester or in the case of distance students at the residential school.

Laboratories are compulsory and all students must pass the laboratory exercise assessments in order to pass the unit (again - a requirement for accreditation).

All information regarding the laboratories / residential school will be provided to the students via the unit website.

Details of the laboratory exercises will be posted on the unit website at the start of the term.

Assessment Due Date

Laboratory Report - Exercises 1, 2 and 3 are due Wednesday Week 7 (11:00 PM), while Laboratory Report - Exercises 4, 5 and 6 are due Wednesday Week 11 (11:00 PM).

Return Date to Students

We strive to return assessments to students within 2 weeks.

Weighting

30%

Minimum mark or grade

A minimum of 50% must be attained for the laboratory exercises in order to pass the unit.

Assessment Criteria

Laboratory exercises will be graded using the following criteria:

- Correct description of laboratory concepts and procedures;
- Correct calculations, analysis / design and thinking;
- Photographic evidence that circuits were constructed by the team;
- Correct measurements, answers and units;
- Photographic and other evidence that correct results / measurements were obtained by the team;
- Confirmation of measured results by simulation with NI Multisim;
- Discussion and understanding of laboratory results;
- **Team reports** must be **professional and typed**, including references;
- All laboratory exercises must be attempted.

Referencing Style

- [Harvard \(author-date\)](#)

Submission

Online

Submission Instructions

1) Plagiarism statement and 2) complete typed report, combined as one .pdf file

Learning Outcomes Assessed

- Research the operation of semiconductor devices and identify their practical applications.
- Analyse the operation and application of analogue electronics devices in various applications.
- Design the interface and power supply arrangements for typical electronic systems.
- Analyse the behaviour of electronic circuits using computer-aided tools.
- Interpret function requirements, evaluate design options and prepare project documents for typical applications.
- Construct electronic circuits to a given design and validate their operation.
- Interpret and use electronic engineering terminologies and symbols that conform to Australian Standards.
- Work collaboratively and autonomously to solve problems and record and communicate clearly and professionally the approach used to solve problems.

Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Literacy
- Team Work
- Information Technology Competence

- Cross Cultural Competence
- Ethical practice

4 ASSIGNMENT 2

Assessment Type

Written Assessment

Task Description

This assessment item covers the weekly Topics 5-9.

Individual work is mandatory - this is a take-home test. None of your steps or solutions may be discussed or divulged to a fellow student.

Please refer to the CQU plagiarism policy - a **signed cover page declaring individual work** is required.

The assignment questions will be released on the unit website at least 2 weeks before the assignment is due to be submitted.

To prevent electronic plagiarism, **typed submissions are not acceptable**. Students should scan clear and legible hand written work for online submission as a **PDF** file.

Assessment Due Date

Week 10 Wednesday (25 Sept 2019) 11:00 pm AEST

Return Date to Students

We strive to return assessments to students within 2 weeks.

Weighting

15%

Assessment Criteria

The assignments will be graded using the following criteria:

- Correct analysis and thinking;
- Drawings of circuits, graphs and waveforms;
- Correct answers and units - confirmation of answers by simulation with NI Multisim;
- All working and intermediate steps must be shown with justification of steps taken;
- Assignments must be tidy and legible;
- All questions must be attempted.

Referencing Style

- [Harvard \(author-date\)](#)

Submission

Online

Submission Instructions

1) Plagiarism statement and 2) complete hand-written assignment scanned in together as one .pdf file

Learning Outcomes Assessed

- Research the operation of semiconductor devices and identify their practical applications.
- Analyse the operation and application of analogue electronics devices in various applications.
- Design the interface and power supply arrangements for typical electronic systems.
- Interpret function requirements, evaluate design options and prepare project documents for typical applications.
- Construct electronic circuits to a given design and validate their operation.
- Interpret and use electronic engineering terminologies and symbols that conform to Australian Standards.
- Work collaboratively and autonomously to solve problems and record and communicate clearly and professionally the approach used to solve problems.

Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Literacy
- Information Technology Competence

Examination

Outline

Complete an invigilated examination.

Date

During the examination period at a CQUniversity examination centre.

Weighting

40%

Length

180 minutes

Minimum mark or grade

A minimum of 50% must be attained for the examination in order to pass the unit.

Exam Conditions

Restricted.

Materials

Dictionary - non-electronic, concise, direct translation only (dictionary must not contain any notes or comments).

Calculator - all non-communicable calculators, including scientific, programmable and graphics calculators are authorised

Academic Integrity Statement

As a CQUniversity student you are expected to act honestly in all aspects of your academic work.

Any assessable work undertaken or submitted for review or assessment must be your own work. Assessable work is any type of work you do to meet the assessment requirements in the unit, including draft work submitted for review and feedback and final work to be assessed.

When you use the ideas, words or data of others in your assessment, you must thoroughly and clearly acknowledge the source of this information by using the correct referencing style for your unit. Using others' work without proper acknowledgement may be considered a form of intellectual dishonesty.

Participating honestly, respectfully, responsibly, and fairly in your university study ensures the CQUniversity qualification you earn will be valued as a true indication of your individual academic achievement and will continue to receive the respect and recognition it deserves.

As a student, you are responsible for reading and following CQUniversity's policies, including the [Student Academic Integrity Policy and Procedure](#). This policy sets out CQUniversity's expectations of you to act with integrity, examples of academic integrity breaches to avoid, the processes used to address alleged breaches of academic integrity, and potential penalties.

What is a breach of academic integrity?

A breach of academic integrity includes but is not limited to plagiarism, self-plagiarism, collusion, cheating, contract cheating, and academic misconduct. The Student Academic Integrity Policy and Procedure defines what these terms mean and gives examples.

Why is academic integrity important?

A breach of academic integrity may result in one or more penalties, including suspension or even expulsion from the University. It can also have negative implications for student visas and future enrolment at CQUniversity or elsewhere. Students who engage in contract cheating also risk being blackmailed by contract cheating services.

Where can I get assistance?

For academic advice and guidance, the [Academic Learning Centre \(ALC\)](#) can support you in becoming confident in completing assessments with integrity and of high standard.

What can you do to act with integrity?

**Be Honest**

If your assessment task is done by someone else, it would be dishonest of you to claim it as your own

**Seek Help**

If you are not sure about how to cite or reference in essays, reports etc, then seek help from your lecturer, the library or the Academic Learning Centre (ALC)

**Produce Original Work**

Originality comes from your ability to read widely, think critically, and apply your gained knowledge to address a question or problem