

Profile information current as at 26/04/2024 04:31 am

All details in this unit profile for ENEE14006 have been officially approved by CQUniversity and represent a learning partnership between the University and you (our student). The information will not be changed unless absolutely necessary and any change will be clearly indicated by an approved correction included in the profile.

General Information

Overview

This unit will introduce you to microcontroller basics and their real-world applications. Fundamentals of high-level structured language programming, essential for programming a microcontroller, will be taught in this unit. You will learn about different microcontroller families and their similarities and differences from an application point of view. You will also learn about microcontroller architecture, memory maps, addressing modes, interrupts, timers, counters, and hardware interfacing of a chosen microcontroller. You will learn how to program a microcontroller in a high-level language using an integrated development environment. Advance topics of reading analog inputs, implementation of Universal Synchronous Asynchronous Receiver Transmitter connections with the external world, Pulse Width Modulation, will also be covered in this unit. After learning the fundamentals of hardware interfacing you will practice them in a laboratory using a microcontroller development system based on a specific microcontroller and finally design and prototype a real-world application of the embedded system in your project using the same development system. Online students are required to attend the Residential School.

Details

Career Level: Undergraduate

Unit Level: Level 4 Credit Points: 12

Student Contribution Band: 8

Fraction of Full-Time Student Load: 0.25

Pre-requisites or Co-requisites

Prerequisite: (ENEE13020 Digital Electronics AND ENEE13018 Analogue Electronics) OR ENEX12002 Introductory Electronics

Important note: Students enrolled in a subsequent unit who failed their pre-requisite unit, should drop the subsequent unit before the census date or within 10 working days of Fail grade notification. Students who do not drop the unit in this timeframe cannot later drop the unit without academic and financial liability. See details in the <u>Assessment Policy and Procedure (Higher Education Coursework)</u>.

Offerings For Term 2 - 2018

Mackay

Attendance Requirements

All on-campus students are expected to attend scheduled classes – in some units, these classes are identified as a mandatory (pass/fail) component and attendance is compulsory. International students, on a student visa, must maintain a full time study load and meet both attendance and academic progress requirements in each study period (satisfactory attendance for International students is defined as maintaining at least an 80% attendance record).

Residential Schools

This unit has a Compulsory Residential School for distance mode students and the details are: Click here to see your <u>Residential School Timetable</u>.

Website

This unit has a website, within the Moodle system, which is available two weeks before the start of term. It is important that you visit your Moodle site throughout the term. Please visit Moodle for more information.

Class and Assessment Overview

Recommended Student Time Commitment

Each 12-credit Undergraduate unit at CQUniversity requires an overall time commitment of an average of 25 hours of study per week, making a total of 300 hours for the unit.

Class Timetable

Regional Campuses

Bundaberg, Cairns, Emerald, Gladstone, Mackay, Rockhampton, Townsville

Metropolitan Campuses

Adelaide, Brisbane, Melbourne, Perth, Sydney

Assessment Overview

1. Written Assessment

Weighting: 25%

2. Written Assessment

Weighting: 25% 3. **Portfolio** Weighting: 50%

Assessment Grading

This is a graded unit: your overall grade will be calculated from the marks or grades for each assessment task, based on the relative weightings shown in the table above. You must obtain an overall mark for the unit of at least 50%, or an overall grade of 'pass' in order to pass the unit. If any 'pass/fail' tasks are shown in the table above they must also be completed successfully ('pass' grade). You must also meet any minimum mark requirements specified for a particular assessment task, as detailed in the 'assessment task' section (note that in some instances, the minimum mark for a task may be greater than 50%). Consult the <u>University's Grades and Results Policy</u> for more details of interim results and final grades.

CQUniversity Policies

All University policies are available on the CQUniversity Policy site.

You may wish to view these policies:

- Grades and Results Policy
- Assessment Policy and Procedure (Higher Education Coursework)
- Review of Grade Procedure
- Student Academic Integrity Policy and Procedure
- Monitoring Academic Progress (MAP) Policy and Procedure Domestic Students
- Monitoring Academic Progress (MAP) Policy and Procedure International Students
- Student Refund and Credit Balance Policy and Procedure
- Student Feedback Compliments and Complaints Policy and Procedure
- Information and Communications Technology Acceptable Use Policy and Procedure

This list is not an exhaustive list of all University policies. The full list of University policies are available on the CQUniversity Policy site.

Previous Student Feedback

Feedback, Recommendations and Responses

Every unit is reviewed for enhancement each year. At the most recent review, the following staff and student feedback items were identified and recommendations were made.

Feedback from 'Have your Say' survey

Feedback

The students have appreciated the clear, to the point, and easy to follow assignments.

Recommendation

The assignments will continue to be clear and concise

Feedback from 'Have your Say' survey

Feedback

The students have appreciated the learning material and the depth of knowledge of the teaching staff.

Recommendation

Learning material will further be improved.

Feedback from 'Have your Say' survey

Feedback

The students have appreciated the programming knowledge gained from this unit which even supports someone who does not have prior knowledge in the area to develop into a confident programmer of hardware.

Recommendation

Further improved support for learning will be provided.

Feedback from 'Have your Say' survey

Feedback

Students have highlighted that the assessment criteria in the portfolio did not clearly define the particular project, which leads to several problems in the implementation stage.

Recommendation

The open-ended project allowed the portfolio assessment criteria to have space for any extraordinary projects to score high marks with successful completion. However, in the next offering, the project will be predefined and the portfolio assessment criteria will be clearly specified based on that project as a trial. If students prefer that setup over the openended project, then that model will be continued in the future offerings.

Feedback from 'Have your Say' survey

Feedback

Students were unhappy with the level of support in some campuses.

Recommendation

Unit coordinator will use Zoom to provide additional support for all campuses.

Unit Learning Outcomes

On successful completion of this unit, you will be able to:

- 1. Apply fundamental structured programming knowledge to develop software solutions
- 2. Program a microcontroller to interface with external devices such as analog and digital sensors, actuators and computers
- 3. Analyse and design microcontroller based real-time applications using a given industry standard development system and software tools
- 4. Prototype an embedded microcontroller system for an authentic application
- 5. Communicate professionally using relevant technical terminology, symbols, and diagrams and effectively document design and prototyped solutions
- 6. Work autonomously and as a team member to analyse problems and present solutions.

The Learning Outcomes for this unit are linked with Engineers Australia's **Stage 1 Competency Standard for Professional Engineers**.

Alignment of Learning Outcomes, Assessment and Graduate Attributes Intermediate Graduate Professional Advanced Introductory Level Level Level Level Level Alignment of Assessment Tasks to Learning Outcomes **Assessment Tasks Learning Outcomes** 2 4 5 6 1 - Written Assessment - 25% 2 - Written Assessment - 25% 3 - Portfolio - 50% Alignment of Graduate Attributes to Learning Outcomes **Graduate Attributes Learning Outcomes** 2 1 3 4 5 6 1 - Communication 2 - Problem Solving 3 - Critical Thinking • 4 - Information Literacy • 5 - Team Work 6 - Information Technology Competence 7 - Cross Cultural Competence

Graduate Attributes					Learning Outcomes					
					1	2	3	4	5	6
8 - Ethical practice					•	•	•	•	•	•
9 - Social Innovation										
10 - Aboriginal and Torres Strait Island	der Cultures									
10 - Aboriginal and Torres Strait Island	der Cultures									
Alignment of Assessment Tas		ribut	es							
	ks to Graduate Att	ribut Graduat		ribute	es					
lignment of Assessment Tasl	ks to Graduate Att G	Graduat				6	7	8	9	10
lignment of Assessment Tasl	ks to Graduate Att G	Graduat	te Att			6	7	8	9	10
Alignment of Assessment Task	ks to Graduate Att G	Graduat 1 2	te Att	4			7		9	10

Textbooks and Resources

Textbooks

There are no required textbooks.

IT Resources

You will need access to the following IT resources:

- CQUniversity Student Email
- Internet
- Unit Website (Moodle)
- Access to a document scanner and a software that can create pdf documents.
- Code::Blocks IDE for C programming (freeware from www.codeblocks.org)
- A computer with 9 pin serial port or USB to serial converter, speaker & microphone, Microsoft Windows OS(7 or later) with admin rights to install software, and good internet connectivity
- MPLAB IDE: MPALB X (provided by CQU on lab computers) or later, and MCC18 complier (free download from Microchip website)

Referencing Style

All submissions for this unit must use the referencing style: <u>Harvard (author-date)</u> For further information, see the Assessment Tasks.

Teaching Contacts

Preethi Preethichandra Unit Coordinator d.preethichandra@cqu.edu.au

Schedule

Week 1 - 09 Jul 2018		
Module/Topic	Chapter	Events and Submissions/Topic
 Introduction to Embedded Systems and Microcontrollers Introduction to programming languages Introduction to C language Programming environment 	N/A	
Week 2 - 16 Jul 2018		
Module/Topic	Chapter	Events and Submissions/Topic
Programming in C language - Fundamentals	N/A	
Week 3 - 23 Jul 2018		
Module/Topic	Chapter	Events and Submissions/Topic
 PIC Microcontroller and MPLAB X - Introduction Programming in C language - Essentials I 	N/A	
Week 4 - 30 Jul 2018		
Module/Topic	Chapter	Events and Submissions/Topic
 PIC 18F4321 Memory, input and 		
output • Programming in C language - Essentials II	N/A	
Week 5 - 06 Aug 2018		
Module/Topic	Chapter	Events and Submissions/Topic
 PIC18F4321 Architecture and 		
addressing modes • Programming in C language - Intermediate level	N/A	Assignment 1 - C language programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST
addressing modesProgramming in C language -	N/A	programming Due: Week 5 Friday
addressing modesProgramming in C language - Intermediate level	N/A Chapter	programming Due: Week 5 Friday
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018		programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic		programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018	Chapter	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing I -	Chapter	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing I - Sensors and display units	Chapter	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing I - Sensors and display units Week 7 - 27 Aug 2018	Chapter Chapter N/A	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic Events and Submissions/Topic
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing I - Sensors and display units Week 7 - 27 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing II -	Chapter Chapter N/A Chapter	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic Events and Submissions/Topic
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing I - Sensors and display units Week 7 - 27 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing II - Motors and actuators	Chapter Chapter N/A Chapter	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic Events and Submissions/Topic
addressing modes • Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing I - Sensors and display units Week 7 - 27 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing II - Motors and actuators Week 8 - 03 Sep 2018	Chapter N/A Chapter N/A	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic Events and Submissions/Topic
addressing modes Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing I - Sensors and display units Week 7 - 27 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing II - Motors and actuators Week 8 - 03 Sep 2018 Module/Topic PIC 18F4321 hardware interfacing III - Module/Topic PIC 18F4321 hardware interfacing III -	Chapter N/A Chapter N/A Chapter	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic Events and Submissions/Topic
addressing modes Programming in C language - Intermediate level Vacation Week - 13 Aug 2018 Module/Topic Week 6 - 20 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing I - Sensors and display units Week 7 - 27 Aug 2018 Module/Topic PIC 18F4321 hardware interfacing II - Motors and actuators Week 8 - 03 Sep 2018 Module/Topic PIC 18F4321 hardware interfacing III - timers and counters	Chapter N/A Chapter N/A Chapter	programming Due: Week 5 Friday (10 Aug 2018) 11:55 pm AEST Events and Submissions/Topic Events and Submissions/Topic

Week 10 - 17 Sep 2018		
Module/Topic	Chapter	Events and Submissions/Topic
Project presentation - (for first set of groups)	N/A	
Week 11 - 24 Sep 2018		
Module/Topic	Chapter	Events and Submissions/Topic
Project presentation - (for second set of groups)	N/A	
Week 12 - 01 Oct 2018		
Module/Topic	Chapter	Events and Submissions/Topic
Embedded microcontroller future opportunities	N/A	Team project - Portfolio Due: Week 12 Friday (5 Oct 2018) 11:55 pm AEST
Review/Exam Week - 08 Oct 2018		
Module/Topic	Chapter	Events and Submissions/Topic
Exam Week - 15 Oct 2018		
Module/Topic	Chapter	Events and Submissions/Topic

Term Specific Information

This unit will be offered in term 2 only for Mechatronics students in Mackay campus.

Assessment Tasks

1 Assignment 1 - C language programming

Assessment Type

Written Assessment

Task Description

This assignment is baced on C language programming. The essential C language skills needed for embedded microcontroller programming will be tested here and students should develop their own solutions to the given problems. Try to analyse the system first and develop a concept solution, develop a graphical representation of it first before start coding. All evidence of your own work including a soft copy of your workbook should be provided as evidence. This is an individual assessment item and no teamwork or contribution from others allowed.

Assessment Due Date

Week 5 Friday (10 Aug 2018) 11:55 pm AEST

Return Date to Students

Week 7 Friday (31 Aug 2018)

Marked assignment with feedback. However, there will be no model answers provided.

Weighting

25%

Assessment Criteria

Detailed assessment criteria is in the assignment itself.

This assignment is based on C language programming skills. To obtain full marks students must provide all in detail problem solving and solution development evidences. Each student must have their unique programms developed by themselves. Answer to every problem should include a flowchart/ NS-diagram or other similar graphical representation of the solution. Students should submit the complete working C language code for each question. A clean working program as the answer to a question without evidence of development of it(ex: a soft copy of workbook pages relevant to it) will receive a maximum of 50% of the allocated marks for that question.

Referencing Style

• Harvard (author-date)

Submission

Online

Submission Instructions

Online submission as a single WORD file and complete program listing for each question to be included in the answer. The program listings should be able to compile without modifications when copied and pasted on Code:Blocs compiler's editor.

Learning Outcomes Assessed

Apply fundamental structured programming knowledge to develop software solutions

Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Literacy
- Information Technology Competence
- Ethical practice

2 Assignment 2 - Hardware Programming using CQU PIC Development kit

Assessment Type

Written Assessment

Task Description

This assignment is baced on essential C language programming skills needed for embedded microcontroller programming. The embedded hardware programming skills will be tested here and students should develop their own solutions to the given problems. Try to analyse the system first and develop a concept solution, develop a graphical representation of it first before start coding. All evidence of your own work including a soft copy of your workbook should be provided as evidence.

This assignment is based on PIC 18F4321 and all programs must be developed with MPLAB X IDE and in C language. More technical details will be provided with the assignment.

This is an individual assessment item and no teamwork or contribution from others allowed.

Assessment Due Date

Week 9 Friday (14 Sept 2018) 11:55 pm AEST

Return Date to Students

Week 11 Friday (28 Sept 2018)

Marked assignment with feedback. However, there will be no model answers provided.

Weighting

25%

Assessment Criteria

Detailed assessment criteria is in the assignment itself.

To obtain full marks students must provide all in detail problem solving and solution development evidences. Each student must have their unique programms developed by themselves. Answer to every problem should include a flowchart/ NS-diagram or other similar graphical representation of the solution. Students should submit the complete working C language code for each questionA clean working program as the answer to a question without evidence of development of it(ex: a soft copy of workbook pages relevant to it or explanation os your code) will receive a maximum of 50% of the allocated marks for that question.

Referencing Style

• Harvard (author-date)

Submission

Online

Submission Instructions

Online submission as a single WORD file and complete program listing for each question to be included in the answer. The program listings should be able to compile without modifications when copied and pasted on MPLABX MCC18 C compiler's editor.

Learning Outcomes Assessed

- Program a microcontroller to interface with external devices such as analog and digital sensors, actuators and computers
- Analyse and design microcontroller based real-time applications using a given industry standard development system and software tools
- Prototype an embedded microcontroller system for an authentic application
- Work autonomously and as a team member to analyse problems and present solutions.

Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Literacy
- Information Technology Competence
- Ethical practice

3 Team project - Portfolio

Assessment Type

Portfolio

Task Description

Project Details

This is the major project in this unit and it is an open ended project. The team can decide the project in consultation with the lecturer and tutors and must finalyse it by week 3. The main hardware used is the CQU PIC Development kit and the accesories provided by CQU in around 3rd week of the unit. Students are free to use any sensors, actuators, and accesories to the project at their own cost. Project outcome(developed prototype) will be tested towards the end of the term.

Portfoio Details

Portfolio is an individual submission based on a team project. It must contain the following items and **omission of any of them will result in a Fail Grade**:

- 1. Copy of team technical report on project (jointly developed by the team)
- 2. Individual technical contribution report (ONLY your technical contribution towards the group project)
- 3. Individual programming report (ONLY your programming contributions towards the group project)
- 4. Peer assessment in the prescribed format (format will be available in Moodle)
- 5. Individual workbook (scanned /electronic copy of your workrbook including your had written work)
- 6. Evidence of timely completion of assigned project work packages

Presentation details

Each group does a mid term presentation and final presentation where every member need to actively participate in both of them. The team project presentations will receive instant formative feedback at the end of the presentation. It is expected that all team members be available to answer the questions at the end of their presentation.

Assessment Due Date

Week 12 Friday (5 Oct 2018) 11:55 pm AEST

Return Date to Students

The portfolio will not be returned until the unit grades are released as there is no final examination for this unit.

Weighting

50%

Minimum mark or grade

50% of the allocated marks.

Assessment Criteria

The portfolio marks will be allocated to the project depth, successfulnes of each individual element, successfulnes of integrating all elements into a complete working prototype, and professional documentation related to project. The portfolio marks will be allocated to the level of the project, the level of successful completion, the level of programming techniques and effective memory usages etc. Individual student contribution must be provided by each group and this will be used to allocate marks for each group member for the project.

Detailed description of assessment criteria will be available in Moodle.

Referencing Style

• Harvard (author-date)

Submission

Online

Learning Outcomes Assessed

- Apply fundamental structured programming knowledge to develop software solutions
- Program a microcontroller to interface with external devices such as analog and digital sensors, actuators and computers
- Analyse and design microcontroller based real-time applications using a given industry standard development system and software tools
- Prototype an embedded microcontroller system for an authentic application
- Communicate professionally using relevant technical terminology, symbols, and diagrams and effectively document design and prototyped solutions
- Work autonomously and as a team member to analyse problems and present solutions.

Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Literacy
- Team Work
- Information Technology Competence
- Ethical practice

Academic Integrity Statement

As a CQUniversity student you are expected to act honestly in all aspects of your academic work.

Any assessable work undertaken or submitted for review or assessment must be your own work. Assessable work is any type of work you do to meet the assessment requirements in the unit, including draft work submitted for review and feedback and final work to be assessed.

When you use the ideas, words or data of others in your assessment, you must thoroughly and clearly acknowledge the source of this information by using the correct referencing style for your unit. Using others' work without proper acknowledgement may be considered a form of intellectual dishonesty.

Participating honestly, respectfully, responsibly, and fairly in your university study ensures the CQUniversity qualification you earn will be valued as a true indication of your individual academic achievement and will continue to receive the respect and recognition it deserves.

As a student, you are responsible for reading and following CQUniversity's policies, including the **Student Academic Integrity Policy and Procedure**. This policy sets out CQUniversity's expectations of you to act with integrity, examples of academic integrity breaches to avoid, the processes used to address alleged breaches of academic integrity, and potential penalties.

What is a breach of academic integrity?

A breach of academic integrity includes but is not limited to plagiarism, self-plagiarism, collusion, cheating, contract cheating, and academic misconduct. The Student Academic Integrity Policy and Procedure defines what these terms mean and gives examples.

Why is academic integrity important?

A breach of academic integrity may result in one or more penalties, including suspension or even expulsion from the University. It can also have negative implications for student visas and future enrolment at CQUniversity or elsewhere. Students who engage in contract cheating also risk being blackmailed by contract cheating services.

Where can I get assistance?

For academic advice and guidance, the <u>Academic Learning Centre (ALC)</u> can support you in becoming confident in completing assessments with integrity and of high standard.

What can you do to act with integrity?



Be Honest

If your assessment task is done by someone else, it would be dishonest of you to claim it as your own



Seek Help

If you are not sure about how to cite or reference in essays, reports etc, then seek help from your lecturer, the library or the Academic Learning Centre (ALC)



Produce Original Work

Originality comes from your ability to read widely, think critically, and apply your gained knowledge to address a question or problem