



# ENEX12002 *Introductory Electronics*

## Term 2 - 2021

Profile information current as at 24/04/2024 07:57 pm

All details in this unit profile for ENEX12002 have been officially approved by CQU University and represent a learning partnership between the University and you (our student). The information will not be changed unless absolutely necessary and any change will be clearly indicated by an approved correction included in the profile.

### General Information

#### Overview

This unit will introduce you to the fundamentals of analog and digital electronics. You will learn the theory of operation commonly used in active and passive electronic components such as resistors, capacitors, inductors, diodes, transistors, signal & power amplifiers, oscillators, and Op-amps. This unit will provide you with sufficient knowledge of Boolean algebra necessary to understand digital electronics. You will learn logic gates, combinational logic circuits, logic minimization, flip-flops, counters, shift-registers, memory, and multiplexers. You will develop skills in analysing electronic circuits and modelling of analog and digital circuits using industry-standard simulation software packages. During this unit, you will design analog and digital systems for real-world applications and test them in simulation software. This unit will also provide you with an opportunity to further develop their professional skills such as communication, technical writing, and individual presentations. In this unit, you must complete compulsory practical activities. Refer to the Engineering Undergraduate Course Moodle site for proposed dates.

#### Details

Career Level: *Undergraduate*

Unit Level: *Level 2*

Credit Points: 6

Student Contribution Band: 8

Fraction of Full-Time Student Load: 0.125

#### Pre-requisites or Co-requisites

Pre-requisite: MATH11219 Applied Calculus AND ENEG11009 Fundamentals of Energy and Electricity

Important note: Students enrolled in a subsequent unit who failed their pre-requisite unit, should drop the subsequent unit before the census date or within 10 working days of Fail grade notification. Students who do not drop the unit in this timeframe cannot later drop the unit without academic and financial liability. See details in the [Assessment Policy and Procedure \(Higher Education Coursework\)](#).

#### Offerings For Term 2 - 2021

- Mackay
- Mixed Mode

#### Attendance Requirements

All on-campus students are expected to attend scheduled classes – in some units, these classes are identified as a mandatory (pass/fail) component and attendance is compulsory. International students, on a student visa, must maintain a full time study load and meet both attendance and academic progress requirements in each study period (satisfactory attendance for International students is defined as maintaining at least an 80% attendance record).

#### Website

[This unit has a website, within the Moodle system, which is available two weeks before the start of term. It is important that you visit your Moodle site throughout the term. Please visit Moodle for more information.](#)

## Class and Assessment Overview

### Recommended Student Time Commitment

Each 6-credit Undergraduate unit at CQUniversity requires an overall time commitment of an average of 12.5 hours of study per week, making a total of 150 hours for the unit.

### Class Timetable

#### [Regional Campuses](#)

Bundaberg, Cairns, Emerald, Gladstone, Mackay, Rockhampton, Townsville

#### [Metropolitan Campuses](#)

Adelaide, Brisbane, Melbourne, Perth, Sydney

### Assessment Overview

#### 1. **Written Assessment**

Weighting: 20%

#### 2. **Written Assessment**

Weighting: 20%

#### 3. **Practical and Written Assessment**

Weighting: 20%

#### 4. **Practical and Written Assessment**

Weighting: 20%

#### 5. **Practical and Written Assessment**

Weighting: 20%

### Assessment Grading

This is a graded unit: your overall grade will be calculated from the marks or grades for each assessment task, based on the relative weightings shown in the table above. You must obtain an overall mark for the unit of at least 50%, or an overall grade of 'pass' in order to pass the unit. If any 'pass/fail' tasks are shown in the table above they must also be completed successfully ('pass' grade). You must also meet any minimum mark requirements specified for a particular assessment task, as detailed in the 'assessment task' section (note that in some instances, the minimum mark for a task may be greater than 50%). Consult the [University's Grades and Results Policy](#) for more details of interim results and final grades.

## CQUniversity Policies

**All University policies are available on the [CQUniversity Policy site](#).**

You may wish to view these policies:

- Grades and Results Policy
- Assessment Policy and Procedure (Higher Education Coursework)
- Review of Grade Procedure
- Student Academic Integrity Policy and Procedure
- Monitoring Academic Progress (MAP) Policy and Procedure – Domestic Students
- Monitoring Academic Progress (MAP) Policy and Procedure – International Students
- Student Refund and Credit Balance Policy and Procedure
- Student Feedback – Compliments and Complaints Policy and Procedure
- Information and Communications Technology Acceptable Use Policy and Procedure

This list is not an exhaustive list of all University policies. The full list of University policies are available on the [CQUniversity Policy site](#).

## Previous Student Feedback

### Feedback, Recommendations and Responses

Every unit is reviewed for enhancement each year. At the most recent review, the following staff and student feedback items were identified and recommendations were made.

#### Feedback from Formal unit evaluation response.

**Feedback**

Add more example questions from the textbook and discuss them in the live class sessions.

**Recommendation**

Most of the theory parts will be available as prerecorded lectures which students can follow prior to the weekly live tutorial session. This will free up some additional time to discuss more questions from the textbook during the live tutorial sessions.

#### Feedback from Self-reflection.

**Feedback**

Interaction and participation of students in the online class were very minimal.

**Recommendation**

More interactive content such as live demos and simulations will be included in the live class sessions. Informal assessment methods will be added to the pre-recorded lectures to track students' learning outcomes. Students will be encouraged to use communication channels such as Microsoft Teams to interact with the lecturers and other students.

## Unit Learning Outcomes

On successful completion of this unit, you will be able to:

1. Describe the operation of semiconductor devices and basic analog electronic circuit building blocks
2. Discuss digital number systems and their hardware implementation in digital information processing systems
3. Analyse the operation of analogue and digital electronic circuits by applying industry-standard simulation tools
4. Test the operation of given analog and digital electronic circuits to validate their operation
5. Design analog and digital electronic circuits to solve real-world problems by Interpreting functional requirements and circuit options
6. Communicate professionally using electronic engineering terminology, symbols and diagrams that conform to Australian and international standards.

The Learning Outcomes for this unit are linked with the Engineers Australia Stage 1 Competency Standards for Professional Engineers in the areas of 1. Knowledge and Skill Base, 2. Engineering Application Ability and 3. Professional and Personal Attributes at the following levels:

Introductory 1.4 Discernment of knowledge development and research directions within the engineering discipline. (LO: 4N 5N ) 1.5 Knowledge of engineering design practice and contextual factors impacting the engineering discipline. (LO: 4N 5N ) 1.6 Understanding of the scope, principles, norms, accountabilities and bounds of sustainable engineering practice in the specific discipline. (LO: 6N ) 3.1 Ethical conduct and professional accountability. (LO: 6N ) 3.3 Creative, innovative and pro-active demeanour. (LO: 6N ) 3.5 Orderly management of self, and professional conduct. (LO: 6N ) Intermediate 1.1 Comprehensive, theory-based understanding of the underpinning natural and physical sciences and the engineering fundamentals applicable to the engineering discipline. (LO: 1I 2I ) 1.2 Conceptual understanding of the mathematics, numerical analysis, statistics, and computer and information sciences which underpin the engineering discipline. (LO: 1I 2I 3I ) 1.3 In-depth understanding of specialist bodies of knowledge within the engineering discipline. (LO: 1I 2I 3I 4N 5N ) 2.1 Application of established engineering methods to complex engineering problem solving. (LO: 3I 4I 5I ) 2.4 Application of systematic approaches to the conduct and management of engineering projects. (LO: 4N 5I ) 3.2 Effective oral and written communication in professional and lay domains. (LO: 6I ) 3.4 Professional use and management of information. (LO: 6I ) Advanced 2.2 Fluent application of engineering techniques, tools and resources. (LO: 3I 4I 5A ) 2.3 Application of systematic engineering synthesis and design processes. (LO: 4I 5A )

Note: LO refers to the Learning Outcome number(s) which link to the competency and the levels: N - Introductory, I - Intermediate and A - Advanced. Refer to the Engineering Undergraduate Course Moodle site for further information on the Engineers Australia's Stage 1 Competency Standard for Professional Engineers and course level mapping information <https://moodle.cqu.edu.au/course/view.php?id=1511>

## Alignment of Learning Outcomes, Assessment and Graduate Attributes



### Alignment of Assessment Tasks to Learning Outcomes

Assessment Tasks	Learning Outcomes					
	1	2	3	4	5	6
1 - Written Assessment - 20%	•					
2 - Written Assessment - 20%		•				
3 - Practical and Written Assessment - 20%	•	•		•		
4 - Practical and Written Assessment - 20%			•		•	•

Assessment Tasks	Learning Outcomes					
	1	2	3	4	5	6
5 - Practical and Written Assessment - 20%			•		•	•

### Alignment of Graduate Attributes to Learning Outcomes

Graduate Attributes	Learning Outcomes					
	1	2	3	4	5	6
1 - Communication						•
2 - Problem Solving		•	•	•	•	
3 - Critical Thinking			•	•	•	
4 - Information Literacy		•	•			•
5 - Team Work						
6 - Information Technology Competence			•		•	
7 - Cross Cultural Competence						
8 - Ethical practice						•
9 - Social Innovation						
10 - Aboriginal and Torres Strait Islander Cultures						

### Alignment of Assessment Tasks to Graduate Attributes

Assessment Tasks	Graduate Attributes									
	1	2	3	4	5	6	7	8	9	10
1 - Written Assessment - 20%		•		•						
2 - Written Assessment - 20%		•		•						
3 - Practical and Written Assessment - 20%		•	•	•						
4 - Practical and Written Assessment - 20%	•	•	•			•		•		
5 - Practical and Written Assessment - 20%	•	•	•			•		•		

### Textbooks and Resources

## Textbooks

ENEX12002

### Prescribed

#### **Analog Fundamentals: A Systems Approach VitalSource eBook**

Edition: 1 (2012)

Authors: Thomas L. Floyd & David M. Buchla

Pearson

NJ , USA

ISBN: 9780133109016

Binding: eBook

ENEX12002

### Prescribed

#### **Digital Fundamentals Global Edition VitalSource eBook**

11th Global edition (2014)

Authors: Thomas L Floyd

Pearson

USA

ISBN: 9781292075990

Binding: eBook

ENEX12002

### Prescribed

#### **TMKIT**

Edition: 1 (2021)

CQU-SET

Binding: Other

ENEX12002

### Prescribed

#### **TMKITU**

Edition: 1 (2021)

CQU-SET

Binding: Other

### Additional Textbook Information

Distance students have the following options for completing the lab component of this unit:

1. Physically attend labs (these are scheduled in blocks) scheduled in the Mackay campus (please refer to time table for the dates )

2. If you can self-supply the equipment and the components required for conducting the labs as listed below, you do not need to purchase a lab kit. You can complete the labs at home without attending the scheduled labs.

You need to purchase TMKIT which has the following items in case you are unable to do 1 or 2 above, please purchase TMKIT (this kit has brand new equipment) or TMKITU (a limited number of TMKITUs are available which include used equipment on campus before. TMKITU comes with a replacement warranty from the school of engineering and technology).

These kits will have the components required to complete the labs of this unit from home.

Inclusions in TMKIT/TMKITU

1 12VAC Plug Pack Power Supply

1 3D Printed Component Box

4 4mm Banana Plug to Test Hook Clip Test Lead Cable

1 BNC Male Plug Q9 to Dual Hook Clip Test Probe Cable Leads (2 leads will come with scope)

1 Breadboard

1 A3 Box for Australia Post Tough Bag

1 Multimeter - True RMS, with Leads

1 Power Supply - 12VAC / 5VDC and -15V/0/15V

1 USB Cable for Oscilloscope (included with Pico Scope)

1 USB Oscilloscope (Pico will include two leads)

Electronics components (Refer to the labs class documentation for the required components)

[View textbooks at the CQUniversity Bookshop](#)

## IT Resources

You will need access to the following IT resources:

- CQUniversity Student Email
- Internet
- Unit Website (Moodle)
- Pdf creator/scanner
- MS Office (mainly Word and Powerpoint)
- Zoom Video Conference Application
- Windows 8 or later PC with admin rights to install software and USB port to operate USB Oscilloscope
- NI Multisim Software (license provided by CQU)
- Microsoft Teams - camera and microphone

## Referencing Style

All submissions for this unit must use the referencing style: [Harvard \(author-date\)](#)

For further information, see the Assessment Tasks.

## Teaching Contacts

**Lasi Piyathilaka** Unit Coordinator  
[l.piyathilaka@cqu.edu.au](mailto:l.piyathilaka@cqu.edu.au)

## Schedule

### Week 1 - 12 Jul 2021

Module/Topic	Chapter	Events and Submissions/Topic
Analog Concepts, Diodes and Applications	Book 1: Analog Fundamentals Chapters 3 and 5	Relevant questions and sample problems will be solved every week.

### Week 2 - 19 Jul 2021

Module/Topic	Chapter	Events and Submissions/Topic
Specialized Diodes and Introduction to BJT	Book 1: Analog Fundamentals Chapters 2 and 3	

### Week 3 - 26 Jul 2021

Module/Topic	Chapter	Events and Submissions/Topic
Transistor Amplifiers, Signal and Power Amplification	Book 1: Analog Fundamentals Chapters 3 and 5	

### Week 4 - 02 Aug 2021

Module/Topic	Chapter	Events and Submissions/Topic
Operational Amplifier	Book 1: Analog Fundamentals Chapters 6 and 7	

### Week 5 - 09 Aug 2021

Module/Topic	Chapter	Events and Submissions/Topic
Special Operational Amplifier Circuits Active Filters	Book 1: Analog Fundamentals Chapters 8 and 9	

### Vacation Week - 16 Aug 2021

Module/Topic	Chapter	Events and Submissions/Topic
--------------	---------	------------------------------

**Week 6 - 23 Aug 2021**

Module/Topic	Chapter	Events and Submissions/Topic
Oscillators Voltage Regulators	Book 1: Analog Fundamentals Chapters 10 and 11	<b>Assignment 1</b> Due: Week 6 Monday (23 Aug 2021) 11:55 pm AEST

**Week 7 - 30 Aug 2021**

Module/Topic	Chapter	Events and Submissions/Topic
Number Systems, Operations, and Logic Gates	Book 2: Digital Fundamentals Chapters 1,2, and 3	

**Week 8 - 06 Sep 2021**

Module/Topic	Chapter	Events and Submissions/Topic
Boolean Algebra and Logic Simplification	Book 2: Digital Fundamentals Chapter 4	<b>Design Assignment 1 (Analog)</b> Due: Week 8 Monday (6 Sept 2021) 11:55 pm AEST

**Week 9 - 13 Sep 2021**

Module/Topic	Chapter	Events and Submissions/Topic
Combinational Logic Analysis and Functions of Combinational Logic	Book 2: Digital Fundamentals Chapters 5 and 6	

**Week 10 - 20 Sep 2021**

Module/Topic	Chapter	Events and Submissions/Topic
Latches and Flip-flops	Book 2: Digital Fundamentals Chapter 7	Residential School 20/09/21 -21/09/21 <b>Practicals, Laboratory Exercise, and Report</b> Due: Week 10 Friday (24 Sept 2021) 11:45 pm AEST

**Week 11 - 27 Sep 2021**

Module/Topic	Chapter	Events and Submissions/Topic
Timers and Counters	Book 2: Digital Fundamentals Chapter 7 and 9	<b>Assignment 2</b> Due: Week 11 Monday (27 Sept 2021) 11:55 pm AEST

**Week 12 - 04 Oct 2021**

Module/Topic	Chapter	Events and Submissions/Topic
Shift Registers Data Storage	Book 2: Digital Fundamentals Chapter 8 and 11	

**Review/Exam Week - 11 Oct 2021**

Module/Topic	Chapter	Events and Submissions/Topic
		<b>Design Assignment 2 (Digital)</b> Due: Review/Exam Week Monday (11 Oct 2021) 11:59 pm AEST

**Exam Week - 18 Oct 2021**

Module/Topic	Chapter	Events and Submissions/Topic

## Term Specific Information

Laboratory exercises will be done in block mode during 20/09/2021- 21/09/2021 from 9.00 AM to 5.00 PM at the Mackay campus. Distance students who do not intend to attend these labs classes are required to buy a lab kit from the bookshop or need to self supply the equipment and components needed to complete the lab exercises.

## Assessment Tasks

### 1 Assignment 1

**Assessment Type**

Written Assessment

**Task Description**

This assessment would cover topics from analog fundamentals. The assessment criteria and questions will be provided well before the submission date and would be strictly followed. The students are not expected to draw any waveforms or write any equations in the word editor, instead they can scan a clear and legible handwritten document and submit it as a *pdf* file. Details will be given on the Moodle unit website.

**Assessment Due Date**

Week 6 Monday (23 Aug 2021) 11:55 pm AEST

**Return Date to Students**

Marked assignment with feedback will be provided

**Weighting**

20%

**Assessment Criteria**

1. Calculates the circuit parameters in various analog circuits by understanding their operations.
2. Selects the most suitable semiconductor device for a given analog circuit application
3. Explains the operation of analog circuits by understanding the characteristics of various analog semiconductor devices
4. Uses simulations correctly to analyse the waveforms generated by analog circuits
5. Compares calculated results with simulation results when solving problems related to analog electronics
6. Report is presented to a professional standard by including all workings and circuit outputs

**Referencing Style**

- [Harvard \(author-date\)](#)

**Submission**

Online

**Submission Instructions**

One pdf file

**Learning Outcomes Assessed**

- Describe the operation of semiconductor devices and basic analog electronic circuit building blocks

**Graduate Attributes**

- Problem Solving
- Information Literacy

### 2 Assignment 2

**Assessment Type**

Written Assessment

**Task Description**

This assessment would cover the topics from digital electronics domain. The assessment criteria and questions will be provided well before the submission date and would be strictly followed. The students are not expected to draw any waveforms or write any equations in the word editor, instead they can scan a clear and legible handwritten document and submit it as a *pdf* file. Details will be given on the Moodle unit website.

**Assessment Due Date**

Week 11 Monday (27 Sept 2021) 11:55 pm AEST

**Return Date to Students**

Marked assignment with feedback will be provided

**Weighting**

20%

**Assessment Criteria**

1. Converts digital number system from one form to another by following the conversion rules
2. Identifies the different digital semiconductor devices by their logic symbols.
3. Combines fundamental logic devices to design high-level logic components
4. Determines the output waveform and timing diagrams for a given digital circuit
5. Uses simulations correctly to analyse the waveforms generated by digital circuits
6. Applies digital logic rules to simplify various digital circuits
5. Compares calculated results with simulation results when solving problems related to digital electronics
6. Report is presented to a professional standard by including all workings and circuit outputs

**Referencing Style**

- [Harvard \(author-date\)](#)

**Submission**

Online

**Submission Instructions**

One pdf file

**Learning Outcomes Assessed**

- Discuss digital number systems and their hardware implementation in digital information processing systems

**Graduate Attributes**

- Problem Solving
- Information Literacy

### 3 Practicals, Laboratory Exercise, and Report

**Assessment Type**

Practical and Written Assessment

**Task Description**

This assessment corresponds to lab practicals and collectively covers almost all topics from both the digital and analog domains. There are mainly two labs in total out of which, lab 1 covers the analogue electronics portion and lab 2 covers the digital portion.

The assessment is distributed as per the content and the details and modalities of these practicals will be available from the unit Moodle website. These practicals are compulsory for every student.

**Assessment Due Date**

Week 10 Friday (24 Sept 2021) 11:45 pm AEST

**Return Date to Students**

Marked labs with feedback will be provided within 2 weeks of submission date

**Weighting**

20%

**Minimum mark or grade**

50%

**Assessment Criteria**

1. Correctly connects various analog and digital semiconductor components by referring to pin diagrams, power requirements and connection procedures.
2. Tests the operation of analog and digital circuit devices by connecting various laboratory measurement devices.
3. Uses measurement devices to troubleshoot analog and digital circuits.
4. Configures simulation software to analyse various analog and digital circuits.
6. Report is presented to a professional standard by including all workings and circuit outputs

## Referencing Style

- [Harvard \(author-date\)](#)

## Submission

Online

## Learning Outcomes Assessed

- Describe the operation of semiconductor devices and basic analog electronic circuit building blocks
- Discuss digital number systems and their hardware implementation in digital information processing systems
- Test the operation of given analog and digital electronic circuits to validate their operation

## Graduate Attributes

- Problem Solving
- Critical Thinking
- Information Literacy

# 4 Design Assignment 1 (Analog)

## Assessment Type

Practical and Written Assessment

## Task Description

This assessment relates to the design of a component / device mainly using contents covered in analog portion of the unit. This mainly software based task would be submitted individually by every student. After the submission the students can fabricate it physically but it would not count towards the grades. Details of this assessment will be available on Moodle unit website.

## Assessment Due Date

Week 8 Monday (6 Sept 2021) 11:55 pm AEST

## Return Date to Students

Marked design with feedback will be provided

## Weighting

20%

## Minimum mark or grade

50%

## Assessment Criteria

1. Identifies the most suitable analog components that can be used to design a circuit to solve a real-world problem.
2. Justifies the selection of different circuit components by calculations
2. Explores different analog circuit options by using calculations and simulations.
3. Designs the final circuit by using standard symbols and correct connection methods
4. Simulates the designed circuit to verify the functional requirements.
5. Report is presented to a professional standard by including all workings and circuit outputs

## Referencing Style

- [Harvard \(author-date\)](#)

## Submission

Online

## Submission Instructions

The report should be in PDF format and all the Multisim simulation files need to be submitted to the Moodle site as a zip file.

## Learning Outcomes Assessed

- Analyse the operation of analogue and digital electronic circuits by applying industry-standard simulation tools
- Design analog and digital electronic circuits to solve real-world problems by interpreting functional requirements and circuit options
- Communicate professionally using electronic engineering terminology, symbols and diagrams that conform to Australian and international standards.

## Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Technology Competence
- Ethical practice

## 5 Design Assignment 2 (Digital)

### Assessment Type

Practical and Written Assessment

### Task Description

This assessment relates to the design of a component / device mainly using material covered in digital portion of the unit. This mainly software based task would be submitted individually by every student. After the submission the students can fabricate it physically but it would not count towards the grades. Details of this assessment will be available on Moodle unit website.

### Assessment Due Date

Review/Exam Week Monday (11 Oct 2021) 11:59 pm AEST

### Return Date to Students

After grade release

### Weighting

20%

### Minimum mark or grade

50%

### Assessment Criteria

1. Identifies the most suitable digital components that can be used to design a circuit to solve a real-world problem.
2. Justifies the selection of different circuit components by using logic expressions and truth tables
2. Explores different digital circuit options by using calculations and simulations.
3. Designs the final circuit by using standard symbols and correct connection methods
4. Simulates the designed circuit to verify the functional requirements.
5. Report is presented to a professional standard by including all workings and circuit outputs

### Referencing Style

- [Harvard \(author-date\)](#)

### Submission

Online

### Submission Instructions

The report should be in PDF format and all the Multisim simulation files need to be submitted to the Moodle site as a zip file.

### Learning Outcomes Assessed

- Analyse the operation of analogue and digital electronic circuits by applying industry-standard simulation tools
- Design analog and digital electronic circuits to solve real-world problems by interpreting functional requirements and circuit options
- Communicate professionally using electronic engineering terminology, symbols and diagrams that conform to Australian and international standards.

## Graduate Attributes

- Communication
- Problem Solving
- Critical Thinking
- Information Technology Competence
- Ethical practice

## Academic Integrity Statement

As a CQUniversity student you are expected to act honestly in all aspects of your academic work.

Any assessable work undertaken or submitted for review or assessment must be your own work. Assessable work is any type of work you do to meet the assessment requirements in the unit, including draft work submitted for review and feedback and final work to be assessed.

When you use the ideas, words or data of others in your assessment, you must thoroughly and clearly acknowledge the source of this information by using the correct referencing style for your unit. Using others' work without proper acknowledgement may be considered a form of intellectual dishonesty.

Participating honestly, respectfully, responsibly, and fairly in your university study ensures the CQUniversity qualification you earn will be valued as a true indication of your individual academic achievement and will continue to receive the respect and recognition it deserves.

As a student, you are responsible for reading and following CQUniversity's policies, including the [Student Academic Integrity Policy and Procedure](#). This policy sets out CQUniversity's expectations of you to act with integrity, examples of academic integrity breaches to avoid, the processes used to address alleged breaches of academic integrity, and potential penalties.

### What is a breach of academic integrity?

A breach of academic integrity includes but is not limited to plagiarism, self-plagiarism, collusion, cheating, contract cheating, and academic misconduct. The Student Academic Integrity Policy and Procedure defines what these terms mean and gives examples.

### Why is academic integrity important?

A breach of academic integrity may result in one or more penalties, including suspension or even expulsion from the University. It can also have negative implications for student visas and future enrolment at CQUniversity or elsewhere. Students who engage in contract cheating also risk being blackmailed by contract cheating services.

### Where can I get assistance?

For academic advice and guidance, the [Academic Learning Centre \(ALC\)](#) can support you in becoming confident in completing assessments with integrity and of high standard.

### What can you do to act with integrity?



#### Be Honest

If your assessment task is done by someone else, it would be dishonest of you to claim it as your own



#### Seek Help

If you are not sure about how to cite or reference in essays, reports etc, then seek help from your lecturer, the library or the Academic Learning Centre (ALC)



#### Produce Original Work

Originality comes from your ability to read widely, think critically, and apply your gained knowledge to address a question or problem