



ENEX20001 *Embedded System Design*

Term 1 - 2018

Profile information current as at 17/05/2024 08:36 pm

All details in this unit profile for ENEX20001 have been officially approved by CQUniversity and represent a learning partnership between the University and you (our student). The information will not be changed unless absolutely necessary and any change will be clearly indicated by an approved correction included in the profile.

General Information

Overview

This unit will introduce you to microcontroller basics and their real world applications. You will learn about different microcontroller families and their similarities and differences from an application point of view. You will also learn about microcontroller architecture, memory maps, addressing modes, interrupts, timers, counters, and hardware interfacing of a chosen microcontroller. You will learn how to program a microcontroller in a high level language using an integrated development environment. Advanced topics of reading analog inputs, implementation of USART (Universal Synchronous Asynchronous Receiver Transmitter) connections with external world, PWM (Pulse Width Modulation), will also be covered in this unit. After learning the fundamentals of hardware interfacing you will practice them in a laboratory using a microcontroller development system based on this specific microcontroller and finally design and prototype an authentic application of embedded system in your project using the same development system. Online education students are required to attend the residential school.

Details

Career Level: *Postgraduate*

Unit Level: *Level 8*

Credit Points: 12

Student Contribution Band: 8

Fraction of Full-Time Student Load: 0.25

Pre-requisites or Co-requisites

ENEE14006 Embedded Microcontrollers is an Anti-Requisite for this unit

Important note: Students enrolled in a subsequent unit who failed their pre-requisite unit, should drop the subsequent unit before the census date or within 10 working days of Fail grade notification. Students who do not drop the unit in this timeframe cannot later drop the unit without academic and financial liability. See details in the [Assessment Policy and Procedure \(Higher Education Coursework\)](#).

Offerings For Term 1 - 2018

- Melbourne
- Perth
- Rockhampton

Attendance Requirements

All on-campus students are expected to attend scheduled classes – in some units, these classes are identified as a mandatory (pass/fail) component and attendance is compulsory. International students, on a student visa, must maintain a full time study load and meet both attendance and academic progress requirements in each study period (satisfactory attendance for International students is defined as maintaining at least an 80% attendance record).

Residential Schools

This unit has a Compulsory Residential School for distance mode students and the details are:

Click here to see your [Residential School Timetable](#).

Website

[This unit has a website, within the Moodle system, which is available two weeks before the start of term. It is important that you visit your Moodle site throughout the term. Please visit Moodle for more information.](#)

Class and Assessment Overview

Recommended Student Time Commitment

Each 12-credit Postgraduate unit at CQUniversity requires an overall time commitment of an average of 25 hours of study per week, making a total of 300 hours for the unit.

Class Timetable

[Regional Campuses](#)

Bundaberg, Cairns, Emerald, Gladstone, Mackay, Rockhampton, Townsville

[Metropolitan Campuses](#)

Adelaide, Brisbane, Melbourne, Perth, Sydney

Assessment Overview

1. **Written Assessment**

Weighting: 20%

2. **Practical Assessment**

Weighting: 20%

3. **Practical Assessment**

Weighting: 20%

4. **Portfolio**

Weighting: 40%

Assessment Grading

This is a graded unit: your overall grade will be calculated from the marks or grades for each assessment task, based on the relative weightings shown in the table above. You must obtain an overall mark for the unit of at least 50%, or an overall grade of 'pass' in order to pass the unit. If any 'pass/fail' tasks are shown in the table above they must also be completed successfully ('pass' grade). You must also meet any minimum mark requirements specified for a particular assessment task, as detailed in the 'assessment task' section (note that in some instances, the minimum mark for a task may be greater than 50%). Consult the [University's Grades and Results Policy](#) for more details of interim results and final grades.

CQUniversity Policies

All University policies are available on the [CQUniversity Policy site](#).

You may wish to view these policies:

- Grades and Results Policy
- Assessment Policy and Procedure (Higher Education Coursework)
- Review of Grade Procedure
- Student Academic Integrity Policy and Procedure
- Monitoring Academic Progress (MAP) Policy and Procedure – Domestic Students
- Monitoring Academic Progress (MAP) Policy and Procedure – International Students
- Student Refund and Credit Balance Policy and Procedure
- Student Feedback – Compliments and Complaints Policy and Procedure
- Information and Communications Technology Acceptable Use Policy and Procedure

This list is not an exhaustive list of all University policies. The full list of University policies are available on the [CQUniversity Policy site](#).

Unit Learning Outcomes

On successful completion of this unit, you will be able to:

1. Apply fundamental structured programming knowledge to perform software tasks
2. Program a microcontroller to interface with external devices such as analog and digital sensors, actuators and computers
3. Analyse and design microcontroller based real-time applications using a given industry standard development system and software tools
4. Prototype an embedded microcontroller system for a real world application
5. Communicate professionally using relevant technical terminology, symbols, and diagrams and effectively document design and prototyped solutions
6. Work autonomously and as a team member to analyse problems and present solutions.

Learning outcomes will be linked to Engineers Australia Stage 1 Competency Standard for Professional Engineers.

Alignment of Learning Outcomes, Assessment and Graduate Attributes



Alignment of Assessment Tasks to Learning Outcomes

| Assessment Tasks | Learning Outcomes | | | | | |
|---------------------------------------|-------------------|---|---|---|---|---|
| | 1 | 2 | 3 | 4 | 5 | 6 |
| 1 - Written Assessment - 20% | • | | | | | |
| 2 - Practical Assessment - 20% | | • | | | | |
| 3 - Practical Assessment - 20% | | | • | • | • | • |
| 4 - Portfolio - 40% | • | • | • | • | • | • |

Alignment of Graduate Attributes to Learning Outcomes

| Graduate Attributes | Learning Outcomes | | | | | |
|---|-------------------|---|---|---|---|---|
| | 1 | 2 | 3 | 4 | 5 | 6 |
| 1 - Knowledge | • | • | • | • | • | • |
| 2 - Communication | • | | • | • | • | • |
| 3 - Cognitive, technical and creative skills | | • | • | • | | • |
| 4 - Research | | • | • | • | • | • |
| 5 - Self-management | | | | • | | • |
| 6 - Ethical and Professional Responsibility | • | • | • | • | • | • |
| 7 - Leadership | | | | | | • |

| Graduate Attributes | | Learning Outcomes | | | | | |
|--|--|-------------------|---|---|---|---|---|
| | | 1 | 2 | 3 | 4 | 5 | 6 |
| 8 - Aboriginal and Torres Strait Islander Cultures | | | | | | | |

Alignment of Assessment Tasks to Graduate Attributes

| Assessment Tasks | Graduate Attributes | | | | | | | |
|--------------------------------|---------------------|-------------|-------------|-------------|-------------|-------------|-------------|---|
| | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| 1 - Written Assessment - 20% | <div></div> | <div></div> | <div></div> | | <div></div> | <div></div> | | |
| 2 - Practical Assessment - 20% | <div></div> | <div></div> | <div></div> | <div></div> | <div></div> | <div></div> | | |
| 3 - Practical Assessment - 20% | <div></div> | <div></div> | <div></div> | <div></div> | <div></div> | <div></div> | | |
| 4 - Portfolio - 40% | <div></div> | <div></div> | <div></div> | <div></div> | <div></div> | <div></div> | <div></div> | |

Textbooks and Resources

Textbooks

There are no required textbooks.

IT Resources

You will need access to the following IT resources:

- CQUniversity Student Email
- Internet
- Unit Website (Moodle)
- Access to a document scanner and a software that can create pdf documents.
- Code::Blocks IDE for C programming (freeware from www.codeblocks.org)
- A computer with 9 pin serial port or USB to serial converter, speaker & microphone, Microsoft Windows OS(7 or later) with admin rights to install software, and good internet connectivity
- MPLAB IDE: MPALB X (provided by CQU on lab computers) or later (free download from Microchip website)

Referencing Style

All submissions for this unit must use the referencing style: [Harvard \(author-date\)](#)
For further information, see the Assessment Tasks.

Teaching Contacts

Preethi Preethichandra Unit Coordinator
d.preethichandra@cqu.edu.au

Schedule

Week 1 - 05 Mar 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--|---------|------------------------------|
| <ul style="list-style-type: none"> • Introduction to Embedded Systems and Microcontrollers • Introduction to programming languages • Introduction to C language Programming environment | N/A | |

Week 2 - 12 Mar 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--|---------|------------------------------|
| Programming in C language - Fundamentals | N/A | |

Week 3 - 19 Mar 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--|---------|------------------------------|
| <ul style="list-style-type: none"> • PIC Microcontroller and MPLAB X - Introduction • Programming in C language - Essentials I | N/A | |

Week 4 - 26 Mar 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|---|---------|---|
| <ul style="list-style-type: none"> • PIC 18F4321 Memory, input and output • Programming in C language - Essentials II | N/A | Assignment 1 - C language programming Due: Week 4 Friday (30 Mar 2018) 11:55 pm AEST |

Week 5 - 02 Apr 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--|---------|------------------------------|
| <ul style="list-style-type: none"> • PIC18F4321 Architecture and addressing modes • Programming in C language - Intermediate level | N/A | |

Vacation Week - 09 Apr 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--------------|---------|------------------------------|
| | | |

Week 6 - 16 Apr 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--|---------|------------------------------|
| PIC 18F4321 hardware interfacing I - Sensors and display units | N/A | |

Week 7 - 23 Apr 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--|---------|---|
| PIC 18F4321 hardware interfacing II - Motors and Actuators | N/A | Assignment 2 - Hardware Programming using CQU PIC Development kit Due: Week 7 Friday (27 Apr 2018) 11:55 pm AEST |

Week 8 - 30 Apr 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--|---------|------------------------------|
| PIC 18F4321 hardware interfacing III - timers and counters | N/A | |

Week 9 - 07 May 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--------------|---------|------------------------------|
| | | |

Introduction to Assembly language programming N/A

Submit Your portfolio draft for feedback

Laboratory Practicals Due: Week 9 Friday (11 May 2018) 11:55 pm AEST

Week 10 - 14 May 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--|---------|---|
| Project presentation - (for first set of groups) | N/A | Project testing - first trial (for first set of groups) |

Week 11 - 21 May 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|---|---------|---|
| Project presentation - (for second set of groups) | N/A | Project testing -first trial (for second set of groups) |

Week 12 - 28 May 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|---|---------|--|
| Embedded microcontroller future opportunities | N/A | Project testing - second trial for prototypes failed to perform successfully during the first trial period. Team Project - Portfolio Due: Week 12 Friday (1 June 2018) 11:55 pm AEST |

Review/Exam Week - 04 Jun 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--------------|---------|------------------------------|
|--------------|---------|------------------------------|

Exam Week - 11 Jun 2018

| Module/Topic | Chapter | Events and Submissions/Topic |
|--------------|---------|------------------------------|
|--------------|---------|------------------------------|

Term Specific Information

Assessment Tasks

1 Assignment 1 - C language programming

Assessment Type

Written Assessment

Task Description

This assignment is based on C language programming. The essential C language skills needed for embedded microcontroller programming will be tested here and students should develop their own solutions to the given problems. Try to analyse the system first and develop a concept solution, develop a graphical representation of it first before start coding. All evidence of your own work including a soft copy of your workbook should be provided as evidence. This is an individual assessment item and no teamwork or contribution from others allowed.

Assessment Due Date

Week 4 Friday (30 Mar 2018) 11:55 pm AEST

Return Date to Students

Week 6 Friday (20 Apr 2018)

Marked assignment with feedback. However, there will be no model answers provided.

Weighting

20%

Assessment Criteria

Detailed assessment criteria is in the assignment itself.

This assignment is based on C language programming skills. To obtain full marks students must provide all in detail problem solving and solution development evidences. Each student must have their unique programmes developed by themselves. Answer to every problem should include a flow chart/ NS diagram or other similar graphical representation of the solution. A clean working program as the answer to a question without evidence of development of it (ex: a soft copy of workbook pages relevant to it) will receive a maximum of 50% of the allocated marks for that question.

Referencing Style

- [Harvard \(author-date\)](#)

Submission

Online

Submission Instructions

online submission as a single pdf file and each program listing in a separate file (ex: Q_1.c for question 1) for each question which can directly be run on Code:Blocs compiler without modifications.

Learning Outcomes Assessed

- Apply fundamental structured programming knowledge to perform software tasks

Graduate Attributes

- Knowledge
- Communication
- Cognitive, technical and creative skills
- Self-management
- Ethical and Professional Responsibility

2 Assignment 2 - Hardware Programming using CQU PIC Development kit

Assessment Type

Practical Assessment

Task Description

This assignment is based on essential C language programming skills needed for embedded microcontroller programming. The embedded hardware programming skills will be tested here and students should develop their own solutions to the given problems. Try to analyse the system first and develop a concept solution, develop a graphical representation of it first before start coding. All evidence of your own work including a soft copy of your workbook should be provided as evidence.

This assignment is based on PIC 18F4321 and all programs must be developed with MPLAB X IDE and in C language. More technical details will be provided with the assignment.

This is an individual assessment item and no teamwork or contribution from others allowed.

Assessment Due Date

Week 7 Friday (27 Apr 2018) 11:55 pm AEST

Return Date to Students

Week 9 Friday (11 May 2018)

Marked assignment with feedback. However, there will be no model answers provided.

Weighting

20%

Assessment Criteria

Detailed assessment criteria is in the assignment itself.

To obtain full marks students must provide all in detail problem solving and solution development evidences. Each student must have their unique programmes developed by themselves. Answer to every problem should include a flow chart/ NS diagram or other similar graphical representation of the solution. A clean working program as the answer to a question without evidence of development of it(ex: a soft copy of workbook pages relevant to it) will receive a maximum of 50% of the allocated marks for that question.

Referencing Style

- [Harvard \(author-date\)](#)

Submission

Online

Submission Instructions

online submission as a single pdf file and each program listing in a separate file (ex: Q_1.c for question 1) for each question which can directly be run on MPBAB X compiler without modifications.

Learning Outcomes Assessed

- Program a microcontroller to interface with external devices such as analog and digital sensors, actuators and computers

Graduate Attributes

- Knowledge
- Communication
- Cognitive, technical and creative skills
- Research
- Self-management
- Ethical and Professional Responsibility

3 Laboratory Practicals

Assessment Type

Practical Assessment

Task Description

This is a collection of laboratory practical exercises which each student needs to complete during lab/workshop time. A practical report must be submitted by individuals. Laboratory tasks will be made available during the laboratory classes. This assignment is based on PIC 18F4321 and all programs must be developed with MPLAB X IDE and in C language. More technical details will be provided with the assignment.

This is an individual assessment item and no teamwork or contribution from others allowed.

Assessment Due Date

Week 9 Friday (11 May 2018) 11:55 pm AEST

Return Date to Students

Week 11 Friday (25 May 2018)

Marked assignment with feedback. However, there will be no model answers provided.

Weighting

20%

Assessment Criteria

To obtain full marks students must provide all in detail problem solving and solution development evidences. Each student must have their unique programs developed by themselves. Answer to every problem should include a flow chart/ NS diagram or other similar graphical representation of the solution. A clean working program as the answer to a question without evidence of development of it (ex: a soft copy of workbook pages relevant to it) will receive a maximum of 50% of the allocated marks for that question.

Referencing Style

- [Harvard \(author-date\)](#)

Submission

Online

Submission Instructions

online submission as a single pdf file and each program listing in a separate file (ex: Q_1.c for question 1) for each question which can directly be run on MPBAB X compiler without modifications.

Learning Outcomes Assessed

- Analyse and design microcontroller based real-time applications using a given industry standard development system and software tools
- Prototype an embedded microcontroller system for a real world application
- Communicate professionally using relevant technical terminology, symbols, and diagrams and effectively document design and prototyped solutions
- Work autonomously and as a team member to analyse problems and present solutions.

Graduate Attributes

- Knowledge
- Communication
- Cognitive, technical and creative skills
- Research
- Self-management
- Ethical and Professional Responsibility

4 Team Project - Portfolio

Assessment Type

Portfolio

Task Description

Project Details

This is the major project in this unit and it is an open ended project. The team can decide the project in consultation with the lecturer and tutors and must finalyse it by week 3. The main hardware used is the CQU PIC Development kit and the accesories provided by CQU in around 3rd week of the unit. Students are free to use any sensors, actuators, and accesories to the project at their own cost. Project outcome (developed prototype) will be tested towards the end of the term.

Portfolio Details

Portfolio is an individual submission based on a team project. It must contain the following items and **omission of any of them will result in a Fail Grade**:

1. Copy of team technical report on project (jointly developed by the team)
2. Individual technical contribution report (ONLY your technical contribution towards the group project)
3. Individual programming report (ONLY your programming contributions towards the group project)
4. Peer assessment in the prescribed format (format will be available in Moodle)
5. Individual workbook (scanned /electronic copy of your workbook including your had written work)
6. Evidence of timely completion of assigned project work packages

Presentation details

Each group does a mid term presentation and final presentation where every member need to active participation in both of them. The team project presentations will receive instant formative feedback at the end of the presentation. It is expected that all team members be available to answer the questions at the end of their presentation.

Assessment Due Date

Week 12 Friday (1 June 2018) 11:55 pm AEST

Return Date to Students

The portfolio will not be returned until the unit grades are released as there is no final examination for this unit.

Weighting

40%

Minimum mark or grade

50% of the allocated marks.

Assessment Criteria

The portfolio marks will be allocated to the project depth, successfulnes of each individual element, successfulness of integrating all elements into a complete working prototype, and professional documentation related to project.

The portfolio marks will be allocated to the level of the project, the level of successful completion, the level of programming techniques and effective memory usages etc. Individual student contribution must be provided by each group and this will be used to allocate marks for each group member for the project.

Detailed description of assessment criteria will be available in Moodle.

Referencing Style

- [Harvard \(author-date\)](#)

Submission

Online

Submission Instructions

Submit Portfolio as a single pdf file and a zip file containging all program listings with correct folder structure.

Learning Outcomes Assessed

- Apply fundamental structured programming knowledge to perform software tasks
- Program a microcontroller to interface with external devices such as analog and digital sensors, actuators and

- computers
- Analyse and design microcontroller based real-time applications using a given industry standard development system and software tools
- Prototype an embedded microcontroller system for a real world application
- Communicate professionally using relevant technical terminology, symbols, and diagrams and effectively document design and prototyped solutions
- Work autonomously and as a team member to analyse problems and present solutions.

Graduate Attributes

- Knowledge
- Communication
- Cognitive, technical and creative skills
- Research
- Self-management
- Ethical and Professional Responsibility
- Leadership

Academic Integrity Statement

As a CQUniversity student you are expected to act honestly in all aspects of your academic work.

Any assessable work undertaken or submitted for review or assessment must be your own work. Assessable work is any type of work you do to meet the assessment requirements in the unit, including draft work submitted for review and feedback and final work to be assessed.

When you use the ideas, words or data of others in your assessment, you must thoroughly and clearly acknowledge the source of this information by using the correct referencing style for your unit. Using others' work without proper acknowledgement may be considered a form of intellectual dishonesty.

Participating honestly, respectfully, responsibly, and fairly in your university study ensures the CQUniversity qualification you earn will be valued as a true indication of your individual academic achievement and will continue to receive the respect and recognition it deserves.

As a student, you are responsible for reading and following CQUniversity's policies, including the [Student Academic Integrity Policy and Procedure](#). This policy sets out CQUniversity's expectations of you to act with integrity, examples of academic integrity breaches to avoid, the processes used to address alleged breaches of academic integrity, and potential penalties.

What is a breach of academic integrity?

A breach of academic integrity includes but is not limited to plagiarism, self-plagiarism, collusion, cheating, contract cheating, and academic misconduct. The Student Academic Integrity Policy and Procedure defines what these terms mean and gives examples.

Why is academic integrity important?

A breach of academic integrity may result in one or more penalties, including suspension or even expulsion from the University. It can also have negative implications for student visas and future enrolment at CQUniversity or elsewhere. Students who engage in contract cheating also risk being blackmailed by contract cheating services.

Where can I get assistance?

For academic advice and guidance, the [Academic Learning Centre \(ALC\)](#) can support you in becoming confident in completing assessments with integrity and of high standard.

What can you do to act with integrity?

**Be Honest**

If your assessment task is done by someone else, it would be dishonest of you to claim it as your own

**Seek Help**

If you are not sure about how to cite or reference in essays, reports etc, then seek help from your lecturer, the library or the Academic Learning Centre (ALC)

**Produce Original Work**

Originality comes from your ability to read widely, think critically, and apply your gained knowledge to address a question or problem